

S1C31 Manual errata

ITEM: Clock Supply in SLEEP Mode			
Object manuals	Document codes	Items	Pages
S1C31D50/D51 Technical Manual	413699407	21.3.2 Clock Supply in SLEEP Mode	21-4
<p>(Error)</p> <p>21.3.2 Clock Supply in SLEEP Mode</p> <p>The HWP and SDAC stop operating in SLEEP mode, as the SYSCLK stops. Do not put the IC into SLEEP mode while the HWP is operating.</p>			
<p>(Correct)</p> <p>21.3.2 Clock Supply in HALT, SLEEP Mode</p> <p>The HWP and SDAC stop operating in HALT or SLEEP mode, as the SYSCLK stops. Do not put the IC into HALT or SLEEP mode while the HWP is operating.</p>			