Response when an error occurs due to non-volatile memory rewriting

When erasing or writing to the flash memory built into the S1C17 MCU using the power supply generated inside the MCU, or when rewriting the EEPROM built into the S1C17 MCU, the recommended circuit for the VPP terminal of the MCU connect the capacitors as shown, and disconnect the FLASH_VCC_OUT terminal of ICDmini Ver.2 / 3 and the VPP terminal of the MCU.