

## S1C17 Manual errata

ITEM: Package			
Object manuals	Document codes	Items	Pages
S1C17653 Technical Manual	412355800	Table 1.1.1 Features Shipping form 1	1-2
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		1.3.2 Pin Descriptions	1-5
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S1C17656 Technical Manual	412745100	Table 1.1.1 Features Shipping form 2	1-2
		1.3.1 Pin Configuration Diagram	1-5
		1.3.2 Pin Descriptions	1-6
		24 Package	24-1

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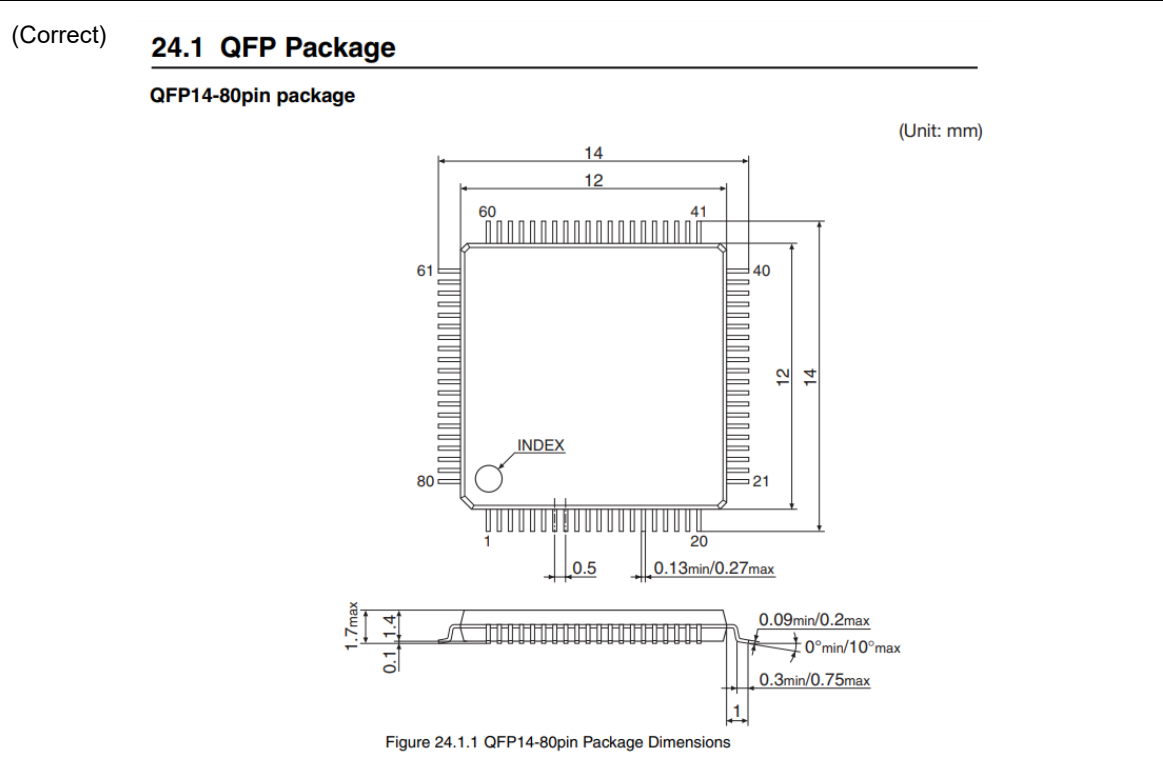
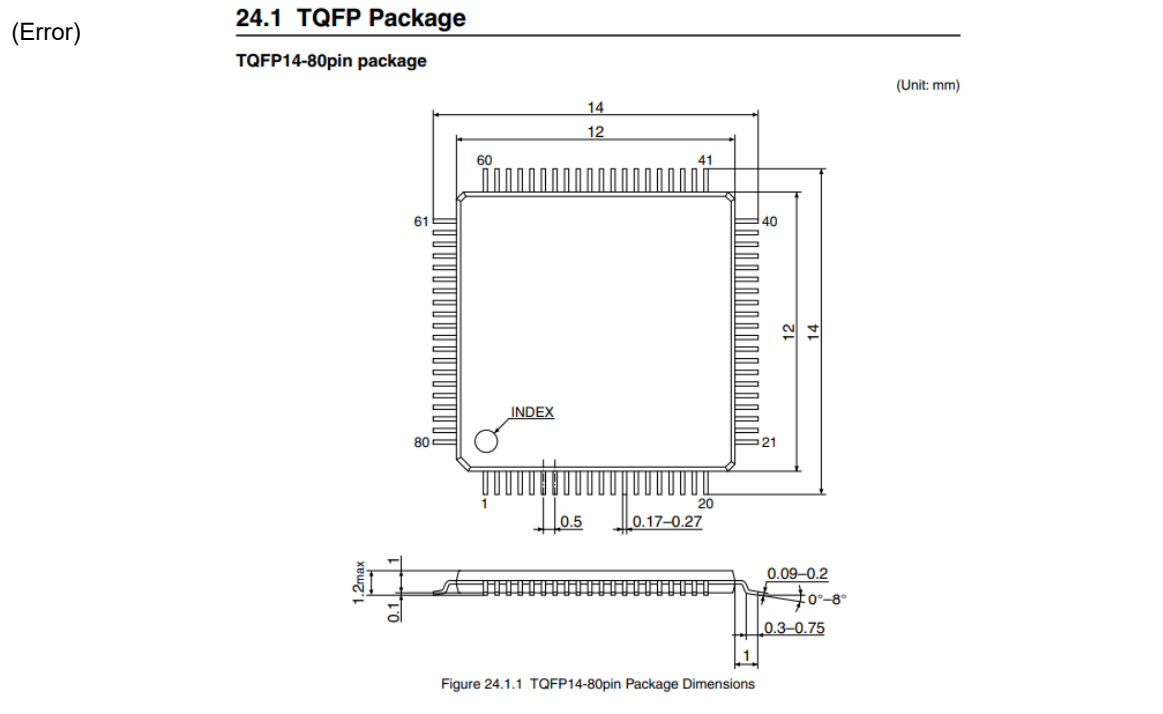
(Error) **TQFP**14-80pin (14 mm × 14 mm × 1 mm, lead pitch: 0.5 mm)

(Correct) **QFP**14-80pin (14 mm × 14 mm × 1.4 mm, lead pitch: 0.5 mm)

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(Error) **TQFP**14-80pin (Correct) **QFP**14-80pin, (Error) TQFP (Correct) QFP

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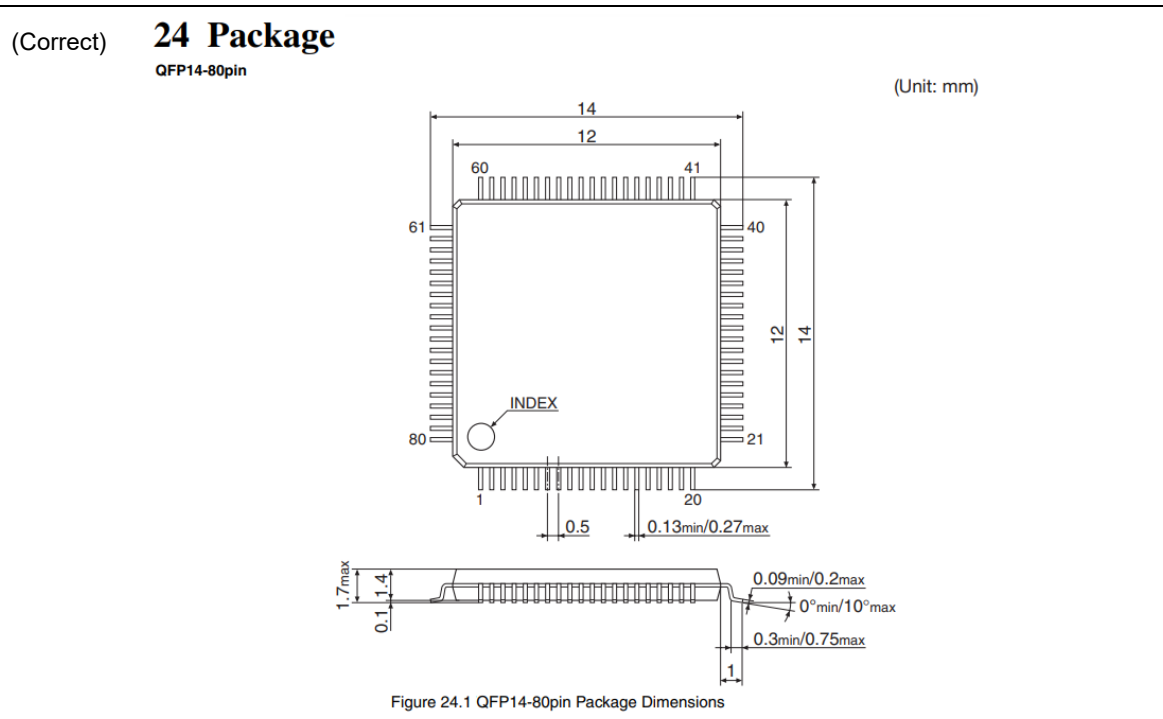
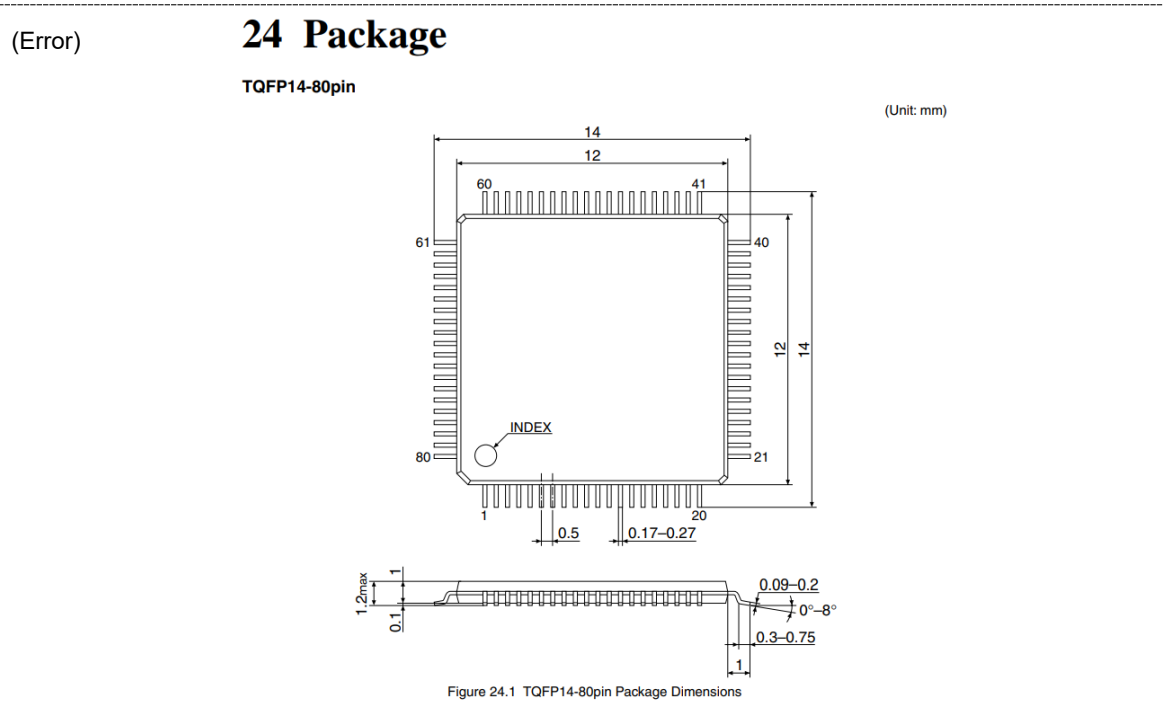
(Error) **TQFP14-80pin** (14 mm × 14 mm × 1 mm, lead pitch: 0.5 mm)

(Correct) **QFP14-80pin** (14 mm × 14 mm × 1.4 mm, lead pitch: 0.5 mm)

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(Error) **TQFP14-80pin** (Correct) **QFP14-80pin** (Error) **TQFP14-80** (Correct) **QFP14-80**

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## S1C17 Family Technical Manual Errata

ITEM External connection for VPP			
Object manual	Document code	Object item	Page
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### Flash Programming

(Error)

(S1C17M01)

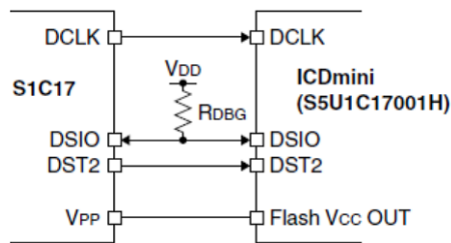


Figure 4.3.3.1 External Connection

(Exclude S1C17M01,S1C17656)

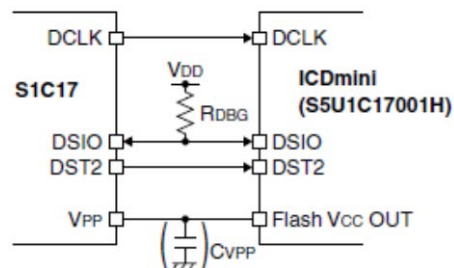


Figure 4.3.3.1 External Connection

The VPP pin must be left open except when programming the Flash memory. However, it is not necessary to disconnect the wire when using ICDmini to supply the VPP power, as ICDmini controls the power supply so that it will be supplied during Flash programming only. CVPP should be connected if the VPP voltage is not

stable due to the effect of the distance between the VPP and Flash VCC OUT or other causes.

(Correct)

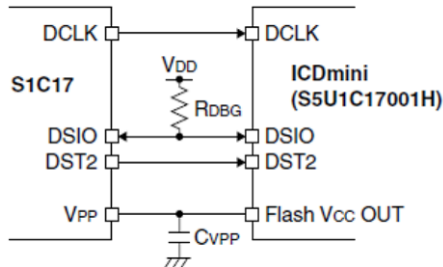


Figure 4.3.3.1 External Connection

The VPP pin must be left open except when programming the Flash memory. However, it is not necessary to disconnect the wire when using ICDmini to supply the VPP voltage, as ICDmini controls the power supply so that it will be supplied during Flash programming only. Be sure to connect CVPP for stabilizing the voltage when the VPP voltage is supplied externally.

#### Flash Programming (Only S1C17656)

(Error)

The S1C17656 supports on-board programming of the Flash memory, it makes it possible to program the Flash memory with the application programs/data by using the debugger through an ICDmini.

(Correct)

The S1C17656 supports on-board programming of the Flash memory, it makes it possible to program the Flash memory with the application programs/data by using the debugger through an ICDmini. Be sure to connect CVPP for stabilizing the voltage when the VPP voltage is supplied externally.

#### Recommended Operating Conditions

(Error)

(S1C17M01)

No description

(Exclude S1C17M01)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Capacitor between VSS and VPP	CVPP	* ※	-	0.1	-	μF

\*※ CVPP should be connected only when the VPP voltage is not stable.

(※ is 4-6)

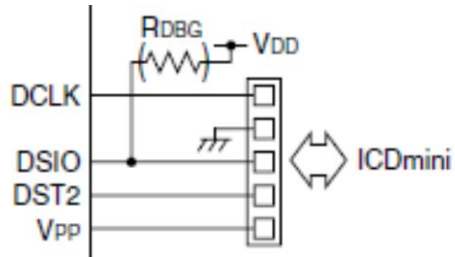
(Correct)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Capacitor between VSS and VPP	CVPP		-	0.1	-	$\mu F$

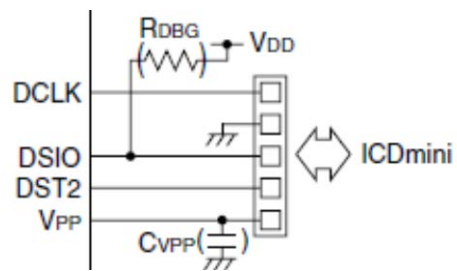
Basic External Connection Diagram

(Error)

(S1C17M01)



(Exclude S1C17M01)



(Correct)

