The VPP pin must be left open except when programming the Flash memory. However, it is not necessary to disconnect the wire when using ICDmini to supply the VPP power, as ICDmini controls the power supply so that it will be supplied during Flash programming only. CVPP should be connected if the VPP voltage is not...
stable due to the effect of the distance between the VPP and Flash VCC OUT or other causes.

(Correct)

![Figure 4.3.3.1 External Connection](image)

The VPP pin must be left open except when programming the Flash memory. However, it is not necessary to disconnect the wire when using ICDmini to supply the VPP voltage, as ICDmini controls the power supply so that it will be supplied during Flash programming only. Be sure to connect CVPP for stabilizing the voltage when the VPP voltage is supplied externally.

Flash Programming (Only S1C17656)

(Error)
The S1C17656 supports on-board programming of the Flash memory, it makes it possible to program the Flash memory with the application programs/data by using the debugger through an ICDmini.

(Correct)
The S1C17656 supports on-board programming of the Flash memory, it makes it possible to program the Flash memory with the application programs/data by using the debugger through an ICDmini. Be sure to connect CVPP for stabilizing the voltage when the VPP voltage is supplied externally.

Recommended Operating Conditions

(Error)
(S1C17M01)
No description

(Exclude S1C17M01)

<table>
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<th>Item</th>
<th>Symbol</th>
<th>Condition</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Unit</th>
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<tbody>
<tr>
<td>Capacitor between VSS and VPP</td>
<td>CVPP</td>
<td>* ※</td>
<td>-</td>
<td>0.1</td>
<td>-</td>
<td>μF</td>
</tr>
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*※ CVPP should be connected only when the VPP voltage is not stable.  
(※ is 4-6)
### Capacitor between VSS and VPP

<table>
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<tr>
<th>Item</th>
<th>Symbol</th>
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<th>Min.</th>
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<td>−</td>
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<td>−</td>
<td>μF</td>
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### Basic External Connection Diagram

(Error)

(Correct)