

## S1C17 Family Technical Manual Errata

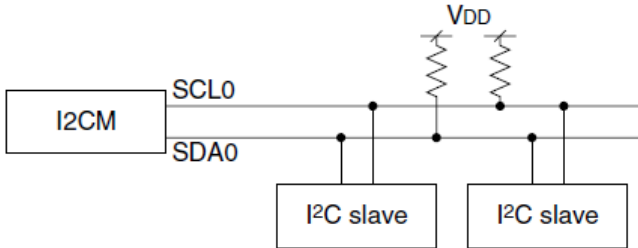
ITEM I2C Slave Input/Output Pins			
Object manual	Document code	Object item	Page
S1C17601 Technical Manual	411805701	21.2 I2C Slave Input/Output Pins	21-2
S1C17611 Technical Manual	411882301	21.2 I2C Slave Input/Output Pins	21-2
S1C17706 Technical Manual	412026401	18.2 I2CS Input/Output Pins	18-1
S1C17002 Technical Manual	411554403	V.3.2 I2C Slave I/O Pins	V-3-2
S1C17003 Technical Manual	411635102	21.2 I2C Slave Input/Output Pins	21-2
S1C17803 Technical Manual	411820401	21.2 I2CS Input/Output Pins	21-2

(Addition)

Note: The pins go to high impedance status when the port function is switched. The SCL and SDA pins do not output a high level, so these lines should be pulled up to VDD with an external pull-up resistor. Be sure to avoid pulling these pins up to a voltage that exceeds the VDD level.

The diagram illustrates the I2C bus configuration. An I2C master is connected to the SDA1 and SCL1 pins of an I2CS (I2C Slave) and an I2C slave. The SDA1 and SCL1 lines are pulled up to VDD with resistors to ensure they are in a high state when not driven by the master.

## S1C17 Family Technical Manual Errata

ITEM I2C Master Input/Output Pins			
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S1C17601 Technical Manual	411805701	20.2 I2C Master Input/Output Pins	20-2
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S1C17701 Technical Manual	411089904	20.2 I2C I/O Pins	20-2
S1C17704 Technical Manual	411511903	20.2 I2C I/O Pins	20-2
S1C17706 Technical Manual	412026401	17.2 I2CM Input/Output Pins	17-1
S1C17001 Technical Manual	411412303	20.2 I2C Input/Output Pins	252
S1C17002 Technical Manual	411554403	V.2.2 I2C Master I/O Pins	V-2-2
S1C17003 Technical Manual	411635102	20.2 I2C Master Input/Output Pins	20-2
S1C17501 Technical Manual	411525602	VI.2.2 I2C I/O Pins	VI-2-2
S1C17801 Technical Manual	411390802	VI.2.2 I2C I/O Pins	VI-2-2
S1C17803 Technical Manual	411820401	20.2 I2CM Input/Output Pins	20-2
<p>(Addition)</p> <p>Note: The pins go to high impedance status when the port function is switched.</p> <p>The SCL and SDA pins do not output a high level, so these lines should be pulled up to VDD with an external pull-up resistor. Be sure to avoid pulling these pins up to a voltage that exceeds the VDD level.</p> 			

## S1C17 Family Technical Manual Errata

ITEM Recommended values for external parts (for the OSC1 oscillator circuit)									
Object manual		Document code		Object item				Page	
S1C17002 Technical Manual		411554403		1.8 Basic External Wiring Diagram				I-8-1	
S1C17003 Technical Manual		411635102		27 Basic External Wiring Diagram				27-1	
<p><b>Page I-8-1</b> S1C17002 Technical Manual</p> <p><b>Page 27-1</b> S1C17003 Technical Manual</p>									
(Error)									
<b>Recommended values for external parts</b>									
External parts the OSC1 oscillator circuit									
Symbol	Resonator	Recommended manufacturer	Frequency [Hz]	Product number	Recommended values				Recommended operating condition
					C <sub>O1</sub> [pF]	C <sub>G1</sub> [pF]	R <sub>f1</sub> [Ω]	R <sub>d1</sub> [Ω]	Temperature range [ ]
X'tal1	Crystal	Epson Toyocom Corporation	32.768k	MC-146(C <sub>L</sub> =7.0pF)	7	7	1M	0	-40 to 85
(Correct)									
<b>Recommended values for external parts</b>									
External parts the OSC1 oscillator circuit									
Symbol	Resonator	Recommended manufacturer	Frequency [Hz]	Product number	Recommended values				Recommended operating condition
					C <sub>O1</sub> [pF]	C <sub>G1</sub> [pF]	R <sub>f1</sub> [Ω]	R <sub>d1</sub> [Ω]	Temperature range [ ]
X'tal1	Crystal	Seiko Epson Corporation	32.768k	MC-146(C <sub>L</sub> =7.0pF)	7	7	10M	0	-40 to 85