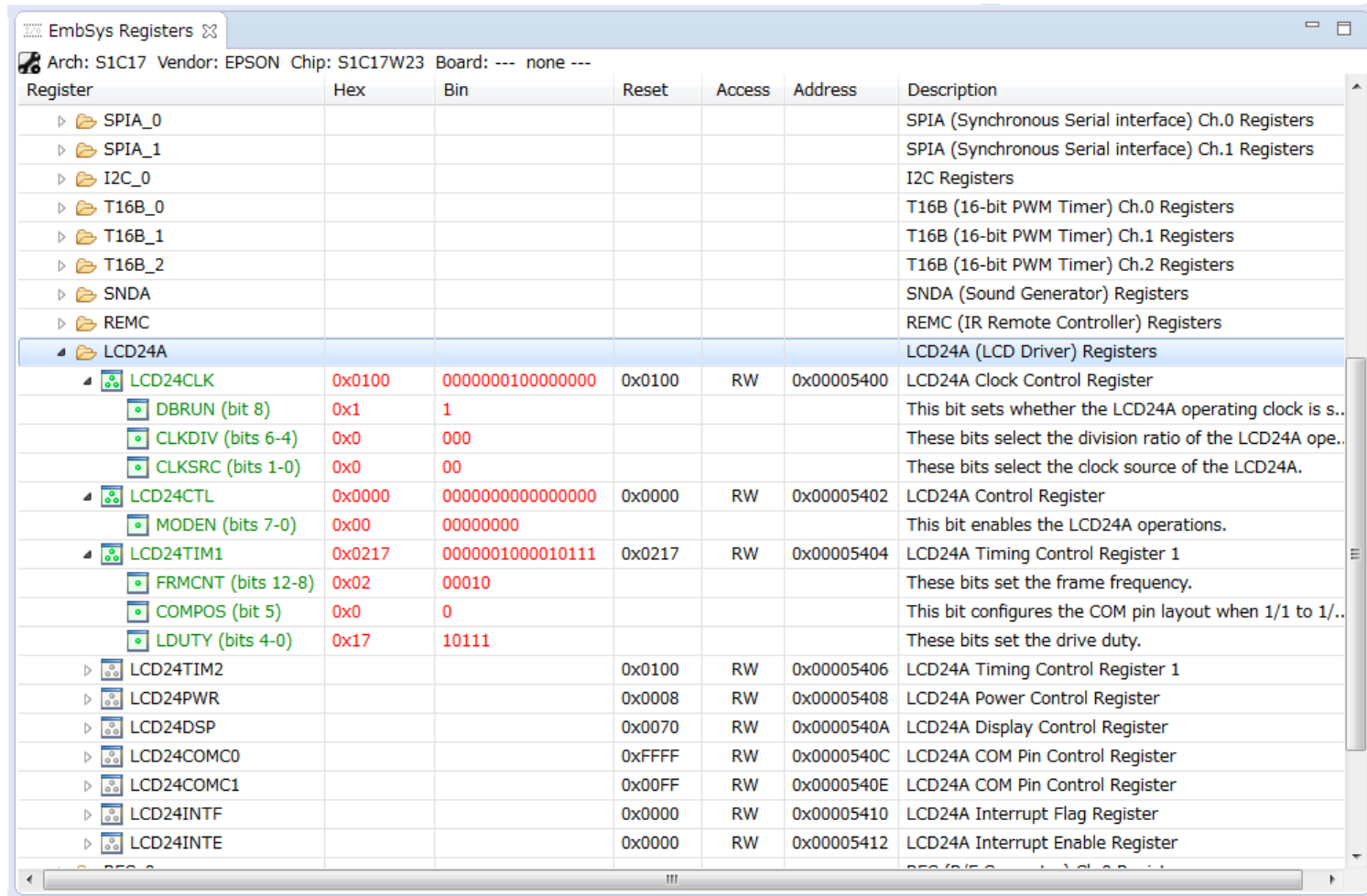


How To Use EmbSys Registers

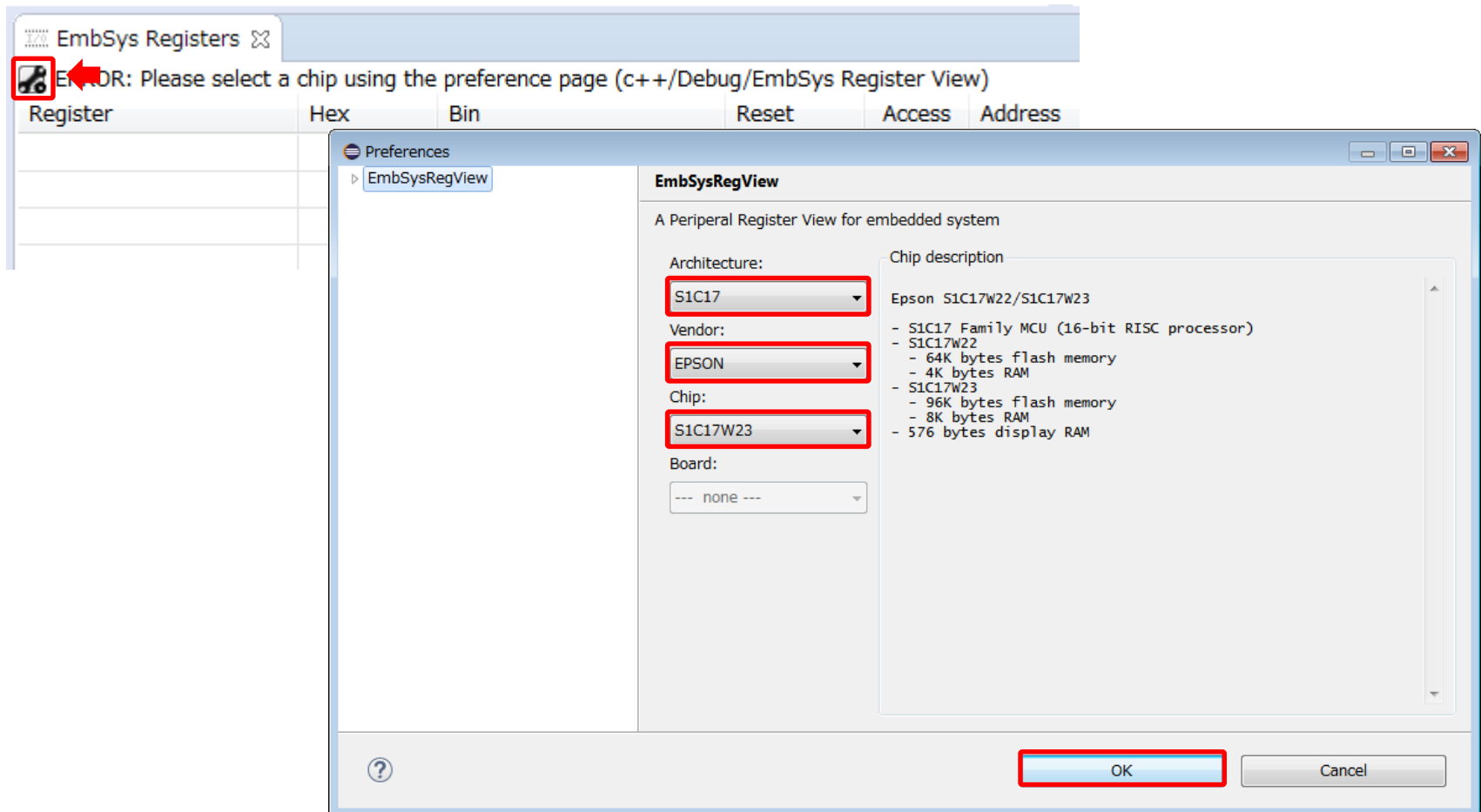
This is useful function for showing register values in debug. It can show not only current values, but also initial values and R/W attributes.



Register	Hex	Bin	Reset	Access	Address	Description
▶ SPIA_0						SPIA (Synchronous Serial interface) Ch.0 Registers
▶ SPIA_1						SPIA (Synchronous Serial interface) Ch.1 Registers
▶ I2C_0						I2C Registers
▶ T16B_0						T16B (16-bit PWM Timer) Ch.0 Registers
▶ T16B_1						T16B (16-bit PWM Timer) Ch.1 Registers
▶ T16B_2						T16B (16-bit PWM Timer) Ch.2 Registers
▶ SNDA						SNDA (Sound Generator) Registers
▶ REMC						REMC (IR Remote Controller) Registers
▲ LCD24A						LCD24A (LCD Driver) Registers
▶ LCD24CLK	0x0100	0000000100000000	0x0100	RW	0x00005400	LCD24A Clock Control Register
▶ DBRUN (bit 8)	0x1	1				This bit sets whether the LCD24A operating clock is s..
▶ CLKDIV (bits 6-4)	0x0	000				These bits select the division ratio of the LCD24A ope..
▶ CLKSRC (bits 1-0)	0x0	00				These bits select the clock source of the LCD24A.
▶ LCD24CTL	0x0000	0000000000000000	0x0000	RW	0x00005402	LCD24A Control Register
▶ MODEN (bits 7-0)	0x00	00000000				This bit enables the LCD24A operations.
▶ LCD24TIM1	0x0217	000000010000010111	0x0217	RW	0x00005404	LCD24A Timing Control Register 1
▶ FRMCNT (bits 12-8)	0x02	00010				These bits set the frame frequency.
▶ COMPOS (bit 5)	0x0	0				This bit configures the COM pin layout when 1/1 to 1/..
▶ LDUTY (bits 4-0)	0x17	10111				These bits set the drive duty.
▶ LCD24TIM2			0x0100	RW	0x00005406	LCD24A Timing Control Register 1
▶ LCD24PWR			0x0008	RW	0x00005408	LCD24A Power Control Register
▶ LCD24DSP			0x0070	RW	0x0000540A	LCD24A Display Control Register
▶ LCD24COMC0			0xFFFF	RW	0x0000540C	LCD24A COM Pin Control Register
▶ LCD24COMC1			0x00FF	RW	0x0000540E	LCD24A COM Pin Control Register
▶ LCD24INTF			0x0000	RW	0x00005410	LCD24A Interrupt Flag Register
▶ LCD24INTE			0x0000	RW	0x00005412	LCD24A Interrupt Enable Register

How To Use EmbSys Registers

2. Setting the target MCU model.
Please push the configuration icon on the upper-left corner.
3. Please set the "Architecture", "Vendor", "Chip" of target MCU.
4. Finally, Push [OK] button.



How To Use EmbSys Registers

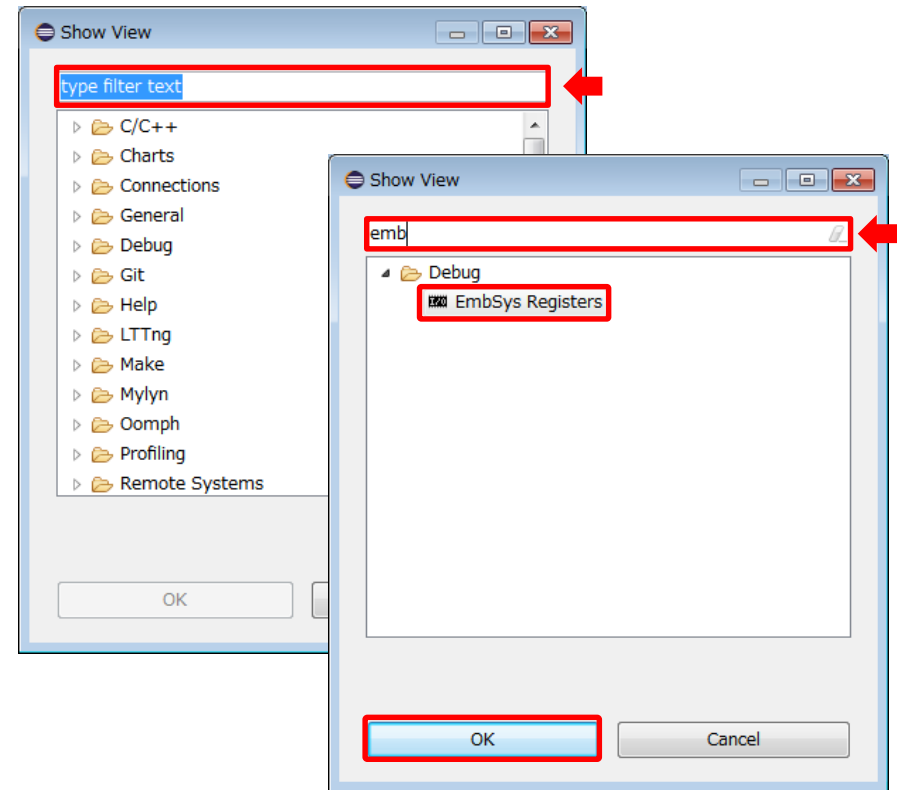
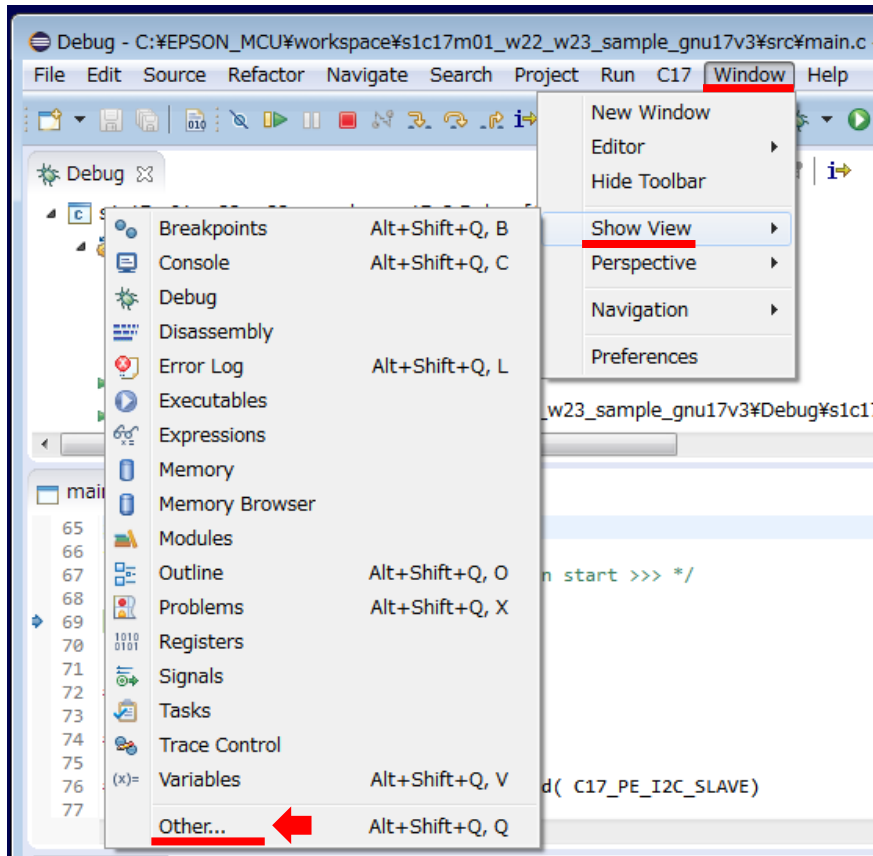
5. Please double click the icons of showing registers, icon will change to "Active (green)".

Register	Hex	Bin	Reset	Access	Address	Description
▶ SPIA_0						SPIA (Synchronous Serial interface) Ch.0 Registers
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▶ LCD24CLK	0x0100	0000000100000000	0x0100	RW	0x00005400	LCD24A Clock Control Register
▶ OBRUN (bit 8)	0x1	1				This bit sets whether the LCD24A operating clock is s...
▶ CLKDIV (bits 6-4)	0x0	000				These bits select the division ratio of the LCD24A ope...
▶ CLKSRC (bits 1-0)	0x0	00				These bits select the clock source of the LCD24A.
▶ LCD24CTL	0x0000	0000000000000000	0x0000	RW	0x00005402	LCD24A Control Register
▶ MODEN (bits 7-0)	0x00	00000000				This bit enables the LCD24A operations.
▶ LCD24TIM1	0x0217	0000001000010111	0x0217	RW	0x00005404	LCD24A Timing Control Register 1
▶ FRCNT (bits 12-8)	0x02	00010				These bits set the frame frequency.
▶ COMPOS (bit 5)	0x0	0				This bit configures the COM pin layout when 1/1 to 1/..
▶ LDUTY (bits 4-0)	0x17	10111				These bits set the drive duty.
▶ LCD24TIM2			0x0100	RW	0x00005406	LCD24A Timing Control Register 1
▶ LCD24PWR			0x0008	RW	0x00005408	LCD24A Power Control Register
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▶ LCD24COMC1			0x00FF	RW	0x0000540E	LCD24A COM Pin Control Register
▶ LCD24INTF			0x0000	RW	0x00005410	LCD24A Interrupt Flag Register
▶ LCD24INTE			0x0000	RW	0x00005412	LCD24A Interrupt Enable Register

How To Use EmbSys Registers

How to open [EmbSys Registers] view in [Debug] perspective.

1. Select [Window]>[Show View]>[Other...]
2. Input "emb" to [type filter text] box in [Show View] window.
3. Select the displayed [EmbSys Registers], and push [OK] button.



How To Use EmbSys Registers

How to add new MCU register definition file.

1. Please insert "*.xml" file which was downloaded from EPSON Web site to below folder in your PC.
"C:\EPSON\GNU17V3\eclipse\plugins\org.eclipse.cdt.embsys\regview.data_0.2.5.r180\data\S1C17\EPSON"
2. Added new MCU can be selected from the configuration icon.

