

This is useful function for showing register values in debug. It can show not only current values, but also initial values and R/W attributes.

📰 EmbSys Registers 🕱						
Arch: S1C17 Vendor: EPSON Chip	: S1C17W23	Board: none				
Register	Hex	Bin	Reset	Access	Address	Description
SPIA_0						SPIA (Synchronous Serial interface) Ch.0 Registers
SPIA_1						SPIA (Synchronous Serial interface) Ch.1 Registers
⊳ 📂 I2C_0						I2C Registers
▷ 📂 T16B_0						T16B (16-bit PWM Timer) Ch.0 Registers
▷ 📂 T16B_1						T16B (16-bit PWM Timer) Ch.1 Registers
▷ 🗁 T16B_2						T16B (16-bit PWM Timer) Ch.2 Registers
SNDA						SNDA (Sound Generator) Registers
REMC						REMC (IR Remote Controller) Registers
🔺 🗁 LCD24A						LCD24A (LCD Driver) Registers
4 🔜 LCD24CLK	0x0100	00000010000000	0x0100	RW	0x00005400	LCD24A Clock Control Register
DBRUN (bit 8)	0x1	1				This bit sets whether the LCD24A operating clock is s
CLKDIV (bits 6-4)	0x0	000				These bits select the division ratio of the LCD24A ope
<ul> <li>CLKSRC (bits 1-0)</li> </ul>	0x0	00				These bits select the clock source of the LCD24A.
4 🔜 LCD24CTL	0x0000	000000000000000000000000000000000000000	0x0000	RW	0x00005402	LCD24A Control Register
<ul> <li>MODEN (bits 7-0)</li> </ul>	0x00	0000000				This bit enables the LCD24A operations.
⊿ 🔜 LCD24TIM1	0x0217	0000001000010111	0x0217	RW	0x00005404	LCD24A Timing Control Register 1
FRMCNT (bits 12-8)	0x02	00010				These bits set the frame frequency.
COMPOS (bit 5)	0x0	0				This bit configures the COM pin layout when 1/1 to 1/
LDUTY (bits 4-0)	0x17	10111				These bits set the drive duty.
LCD24TIM2			0x0100	RW	0x00005406	LCD24A Timing Control Register 1
▷ 📩 LCD24PWR			0x0008	RW	0x00005408	LCD24A Power Control Register
▷ 👶 LCD24DSP			0x0070	RW	0x0000540A	LCD24A Display Control Register
▷ 💼 LCD24COMC0			0xFFFF	RW	0x0000540C	LCD24A COM Pin Control Register
LCD24COMC1			0x00FF	RW	0x0000540E	LCD24A COM Pin Control Register
LCD24INTF			0x0000	RW	0x00005410	LCD24A Interrupt Flag Register
D 🔊 LCD24INTE			0x0000	RW	0x00005412	LCD24A Interrupt Enable Register



# 1. By default, there is [EmbSys Registers] view in [C/C++] perspective.

⊜ C/C++ - Eclipse								
<u>F</u> ile <u>E</u> dit <u>S</u> ource Refac <u>t</u> or <u>N</u> avigate Se <u>a</u> rch <u>P</u> roject <u>R</u> un C17 <u>W</u> indow <u>H</u> elp								
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🎦 Project Explorer 🛛 🖓 🗖								🗄 O 🛛 💿 M 🗐 T 🖓 🗖
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s1c17m01_w22_w23_sample							1	An outline is not available.
Binaries								
Includes								
⊳ 📇 inc								
⊳ 📇 src								
> 👝 Debug								
gdbmini2.ini								
gdbmini3.ini								
gdbsim.ini								
gpdata.ini		The EmbSys Registers St						
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s1c17m01_w22_w23_sar	<u>^</u>							
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- 2. Setting the target MCU model. Please push the configration icon on the upper-left corner.
- 3. Please set the "Architecture", "Vendor", "Chip" of target MCU.
- 4. Finally, Push [OK] button.

📰 EmbSys Registers 🛛								
🔏 E 💭 R: Please select a	chip using t	he preference page	e (c++/Debug/EmbSys I	Register Vie	w)			
Register	Hex	Bin	Reset	Access	Address			
	Prefere	ences /sRegView	EmbSysRegView A Periperal Register View f Architecture: S1C17 Vendor: EPSON Chip: S1C17W23 Board: none	<ul> <li>Chip descr</li> <li>Chip descr</li> <li>S1C17 #</li> <li>S1C17W2</li> <li>G4K tit</li> <li>4K bit</li> <li>96K tit</li> <li>8K bit</li> <li>576 byt</li> </ul>	stem iption amily MCU (16- 22 bytes flash mem /tes RAM 23 bytes flash mem /tes RAM es display RAM	bit RISC processor) ory ory		
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5. Please double click the icons of showing registers, icon will change to "Active (green)".

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▷ 💽 LCD24INTE			0x0000	RW	0x00005412	LCD24A Interrupt Enable Register	-
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#### How to open [EmbSys Registers] view in [Debug] perspective.

- 1. Select [Window]>[Show View]>[Other...]
- 2. Input "emb" to [type filter text] box in [Show View] window.
- 3. Select the displayed [EmbSys Registers], and push [OK] button.





How to add new MCU register diffinition file.

- Please insert "\*.xml" file which was downloaded from EPSON Web site to below folder in your PC. "C:¥EPSON¥GNU17V3¥eclipse¥plugins¥org.eclipse.cdt.embsy sregview.data\_0.2.5.r180¥data¥S1C17¥EPSON¥"
- 2. Added new MCU can be selected from the configulation icon.

EmbSys Registers	8								
ECTDR: Please select a chip using the preference page (c++/Debug/EmbSys Register View)									
Register	Hex	Bin	Reset	Access	Address				