

## D13305 vs. D13700 Comparison Table

Item	Sub-item	S1D13305	S1D13700
Configuration		80 series/68 series CPU types	← Frequency division (1/4, 1/8, 1/16) Direct/indirect interface
Clock	Clock waveform input	External crystal oscillator + Internal crystal oscillator Not available	← Available
Host interface		Asynchronous	Synchronous (Clock input is required during access)
Display memory	External memory Internal memory	Connectable with external SRAM (up to 64K bytes) Not available	Not connectable with external SRAM 32K-byte SRAM is embedded
Character memory	Internal CGROM Internal CGRAM  External CGROM External CGRAM	160 characters (5 x 7 dots) Not available  256 characters (when M0=1) 64 characters (when M0=0)	64 characters (when M0=0) 256 characters (when M0=1) Not available Not available
Display mode	B/W display  Gray scale display	Character mode Graphic mode Character/graphic mixed mode (5 types)	← ← Character/graphic mixed mode (16 types) 2bpp (4-level gray scale) 4bpp (16-level gray scale)
Display dot count	Character mode (1bpp) Character mode (2bpp) Character mode (4bpp) Graphic mode (1bpp) Graphic mode (2bpp) Graphic mode (4bpp)	80 columns x 32 rows + Graphic layer Not available Not available 640 dots x 256 dots x 3 layers Not available Not available	80 columns x 30 rows + Graphic layer 40 columns x 30 rows + Graphic layer 20 columns x 30 rows + Graphic layer 640 dots x 240 dots x 3 layers 320 dots x 240 dots x 3 layers 160 dots x 240 dots x 3 layers
Command	SYSTEMSET GRAYSCALE OVLAY Cursor control (Indirect mode) Cursor control (Direct mode) Smooth scroll function Status flag	M1 Not available MX1, MX2 Automatic cursor move (4 directions: up, down, right and left) Not available  Horizontal direction only Available	Not available Available Redefinable priority overlay ←  Automatic move to the address when accessing the display memory  Not available
Power supply voltage		2.7V ~ 5.5V (Single)	Dual power supply HVDD = 3.3V±0.3V, 5.0V±0.5V (Note: Power supplies are selectable for the host and display separately.) LVDD = 3.3V±0.3V
Max. operating frequency		10MHz at 5.0V 8MHz at 3.3V	60MHz
Package		QFP5-60pin QFP6-60pin Not available	Not available Not available T-QFP13-64pin (Pd-Free)