

S1C17 Family Port Nested Interrupt Application Notes

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1. Overview

The S1C17 Family microcontrollers have an interrupt vector table in which multiple interrupt sources for each peripheral circuit are combined into each interrupt vector. For the port input interrupt, the interrupt sources for all ports are assigned to one interrupt vector. Therefore, controlling only the interrupt controller does not allow nested interrupt handling with prioritized port interrupt sources.

This application note provides the functions that manage port input interrupt priorities and handle nested interrupts.

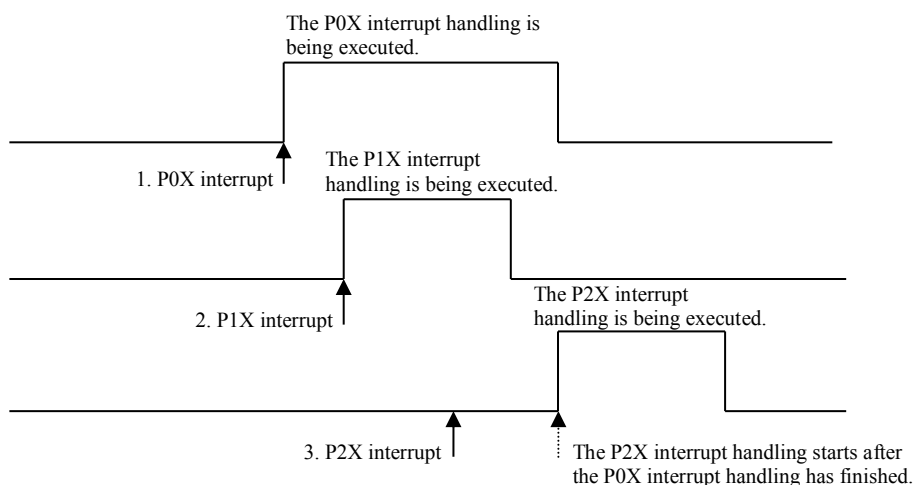
1.1 Functions

A function in this application note sets the interrupt level for each port group (P0X, P1X, P2X, ...).

The interrupt handler function in this application note handles port input interrupts. If a new interrupt having a higher priority occurs while the function is handling an interrupt, the function executes handling of the new interrupt. The interrupt handling being executed currently is temporarily suspended until the handling of new interrupt has finished. If the new interrupt has a lower or the same priority, it will be handled after the handling of the interrupt being executed has finished.

The figure below shows an example of a port input interrupt handling sequence.

Interrupt levels: P0X = 2
P1X = 3
P2X = 1



1. A P0X interrupt occurs.
→ The P0X interrupt handling is executed.
2. A P1X interrupt occurs while the P0X interrupt handling is being executed.
→ Since the interrupt levels have been set as $P0X < P1X$, the P1X interrupt handling is executed.
3. A P2X interrupt occurs while the P1X interrupt handling is being executed.
→ Since the interrupt levels have been set as $P2X < P1X$ and $P2X < P0X$, the P2X interrupt handling is executed after the P0X interrupt handling has finished.

1. Overview

1.2 Folder Configuration

The folder configuration in this package is as follows:

+c17_Port_Nested_Interrupt

+c17_Port_Nested_Interrupt_gnu17v2 : Sample program for GNU17 Ver. 2.x

+c17_Port_Nested_Interrupt_gnu17v3 : Sample program for GNU17 Ver. 3.x

-s1c17_Port_Nested_Interrupt_j.pdf : Manual (Japanese)

-s1c17_Port_Nested_Interrupt_e.pdf : Manual (English)

-License_e.txt : Software license agreement (English)

-README.txt : README file

1.3 File Configuration

The file configuration in this package is as follows:

File name	Description
src/c17_Port_Nested_Interrupt.c	Port nested interrupt handler function
src/c17_Port_Nested_Interrupt.h	Port nested interrupt handler function header definition file
src/main.c	Main function
src/boot.c	Startup module
src/c17_port_m30_m31_m32_m33_m34.c	Sample port driver
src/c17_port_m30_m31_m32_m33_m34.h	Sample port driver header definition file
src/c17_misc.c	Sample MISC driver
src/c17_misc.h	Sample MISC driver header definition file
src/c17_clg.c	Sample CLG driver
src/c17_clg.h	Sample CLG driver header definition file
src/c17_init_config.h	Peripheral circuit configuration header definition file
src/crt0.h	Interrupt vectors and boot function header definition file
inc	CPU register definition file directory
inc/reg	CPU peripheral circuit control register definition file directory

2. Details of Sample Software Functions

This chapter gives a detailed description of the sample software functions.

2.1 Sample Software Specifications

This sample software is implemented for S1C17M33 to execute the following processing:

- Configures the specified ports as input ports that generate an interrupt when the input signal transits from a high level to a low level.
- When an interrupt occurs from an input port, the sample software configures the specified port as an output port that outputs a high level.
- Waits for the input port to be changed to a high level.
- Sets the output port to output a low level.

The table below lists the configuration of the ports used in this sample software.

Table 2.1.1 Port Configuration in Sample Software

Port group	Configuration
P0X	Input interrupt port = P00
	Output port = P07
	Interrupt level = 2
P1X	Input interrupt port = P10
	Output port = P17
	Interrupt level = 3
P2X	Input interrupt port = P20
	Output port = P27
	Interrupt level = 2
P3X	Input interrupt port = P30
	Output port = P37
	Interrupt level = 1

2. Details of Sample Software Functions

2.2 Processing Overview

Main processing

The main function initializes the ports as follows:

- Sets the interrupt level for the P0X port group.
- Enables the P00 port to generate an interrupt at the falling edge of the input signal.
- Sets the P07 port to output a low level.
- Sets the interrupt level for the P1X port group.
- Enables the P10 port to generate an interrupt at the falling edge of the input signal.
- Sets the P17 port to output a low level.
- Sets the interrupt level for the P2X port group.
- Enables the P20 port to generate an interrupt at the falling edge of the input signal.
- Sets the P27 port to output a low level.
- Sets the interrupt level for the P3X port group.
- Enables the P30 port to generate an interrupt at the falling edge of the input signal.
- Sets the P37 port to output a low level.

Interrupt handling (P0X)

The interrupt handler calls the port group interrupt function (`intInputP0x`) that performs the processing shown below.

- Sets the P07 port to output a high level.
- Waits for the P00 port to be changed to a high level.
- Sets the P07 port to output a low level.

The interrupt functions for other port groups (`intInputP1x`, `intInputP2x`, ...) performs the same processing as above except for the port numbers.

2.3 Using in Applications

This section describes how to import the sample software into your application project.

1. Add header file to project
Add “c17_Port_Nested_Interrupt.c/c17_Port_Nested_Interrupt.h” to the project.
2. Declare header file to be included
Add the #include declaration shown below in the source file that refers to the sample functions.

```
#include "c17_port_nested_interrupt.h"
```

3. Register to vector table

When GNU17 Ver. 2.x is used

Register the interrupt function “c17portNestedInterruptPortInput” as the PORT interrupt vector.

boot.c

```
#include "c17_port_nested_interrupt.h"
/** vector table */
func *const vector[] = {
    /* ----- Non-Maskable Interrupts ----- */
    VECTOR(boot), // 0 00 Reset
    VECTOR(intAddrErr), // 1 04 Address misaligned
    VECTOR(intReserved), // 2 08 NMI
    VECTOR(emu_copro_process), // 3 0C Co-processor emulation

    /* ----- Maskable Interrupts ----- */
    VECTOR(c17intSvd), // 4 10 SVD3 interrupt
    VECTOR(c17portNestedInterruptPortInput), // 5 14 PORT interrupt
};
```

When GNU17 Ver. 3.x is used

When using the sample port driver, comment out the vector handler definition shown below.

c17_port_m30_m31_m32_m33_m34.c

```
/* --- interrupt handler (GNU17v3 Only) --- */
#ifdef CRT0_LINKED
//C17_INTERRUPT_HANDLER(_vector05_handler, c17intPortInput); // comment out
#endif /* CRT0_LINKED */
```


2. Details of Sample Software Functions

4. Add user interrupt processing

Add user processing routines to the interrupt functions (intInputP0x, intInputP1x, intInputP2x, ...) written in the “c17_port_nested_interrupt.c” file.

```
void intInputP0x(void)
{
    unsigned short interruptFlag;

    interruptFlag = pxintf_get[C17_PORT_GROUP_P0X]; //Obtain interrupt source storage variable
    pxintf_get[C17_PORT_GROUP_P0X] = 0; //Clear interrupt source storage variable
    asm("ei"); //Enable nested interrupts
    if(interruptFlag & 0x01){
        /**
        * @todo add interrupt function of P00 //Add user processing routine
        */
    }
    if(interruptFlag & 0x02){
        /**
        * @todo add interrupt function of P01 //Add user processing routine
        */
    }
};
```

5. Set interrupt level for each port group

Use the “c17portNestedInterruptSetInterruptLevel” function to set the interrupt level for the port group (P0X, P1X, P2X, ...) of which input interrupt is used.

```
c17portNestedInterruptSetInterruptLevel(C17_PORT_GROUP_P0X, 2);
c17enableIntP0x(0, C17_PORT_INT_EDGE_FALLING);

c17portNestedInterruptSetInterruptLevel(C17_PORT_GROUP_P1X, 3);
c17enableIntP1x(0, C17_PORT_INT_EDGE_FALLING);

c17portNestedInterruptSetInterruptLevel(C17_PORT_GROUP_P2X, 2);
c17enableIntP2x(0, C17_PORT_INT_EDGE_FALLING);

c17portNestedInterruptSetInterruptLevel(C17_PORT_GROUP_P3X, 1);
c17enableIntP3x(0, C17_PORT_INT_EDGE_FALLING);
```

3. Function Specifications

This chapter describes the functions in this sample software.

Port group interrupt level setting function

Function name	c17portNestedInterruptSetInterruptLevel	
Arguments	unsigned short portGr	Port group (P0X = 0, P1X = 1, ...)
	unsigned short portIntLevel	Interrupt level from 1 to 4
Return value	unsigned short	EXIT_SUCCESS: Successful EXIT_FAILURE: Failed
Description	This function sets the interrupt level for the port group specified with the argument.	
Remarks	The effective range of the interrupt level is between 1 to 4.	

Port interrupt handler function

Function name	c17portNestedInterruptPortInput
Argument	None
Return value	None
Description	This function is the port interrupt handler that calls an interrupt function (intInputP0x, intInputP1x, intInputP2x, ...) according to the port group that generates an interrupt.
Remarks	When GNU17 Ver. 2.x is used, register this function as the PORT interrupt vector.

Port group interrupt functions

Function names	intInputP0x, intInputP1x, intInputP2x, ...
Argument	None
Return value	None
Description	These functions are called from the port interrupt handler function and perform the following processing: <ol style="list-style-type: none"> 1. Obtains the interrupt source from the interrupt source storage variable. 2. Clears the interrupt source storage variable. 3. Enables nested port interrupts. 4. Executes the processing for the port that generates an interrupt (user defined routine).
Remarks	The processing routine for each port should be described by the user.

4. Operating Time

4. Operating Time

The time shown below will be taken after a hardware interrupt occurs from an input port until the port interrupt function (intInputPxx) is called.

Time: 228 μ s

Reference

Normal S1C17M33 sample software

Time: 208 μ s

Conditions

Operating clock: IOOSC = 700 kHz

Access wait cycle: FLASHCWAIT.RDWAIT[1:0] bits = 0x0 (no wait)

Input interrupt port: P10

Revision History

Attachment-1

Rev. No.	Date	Page	Category	Contents
Rev 1.0	2017/07/24	All	New	New establishment

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