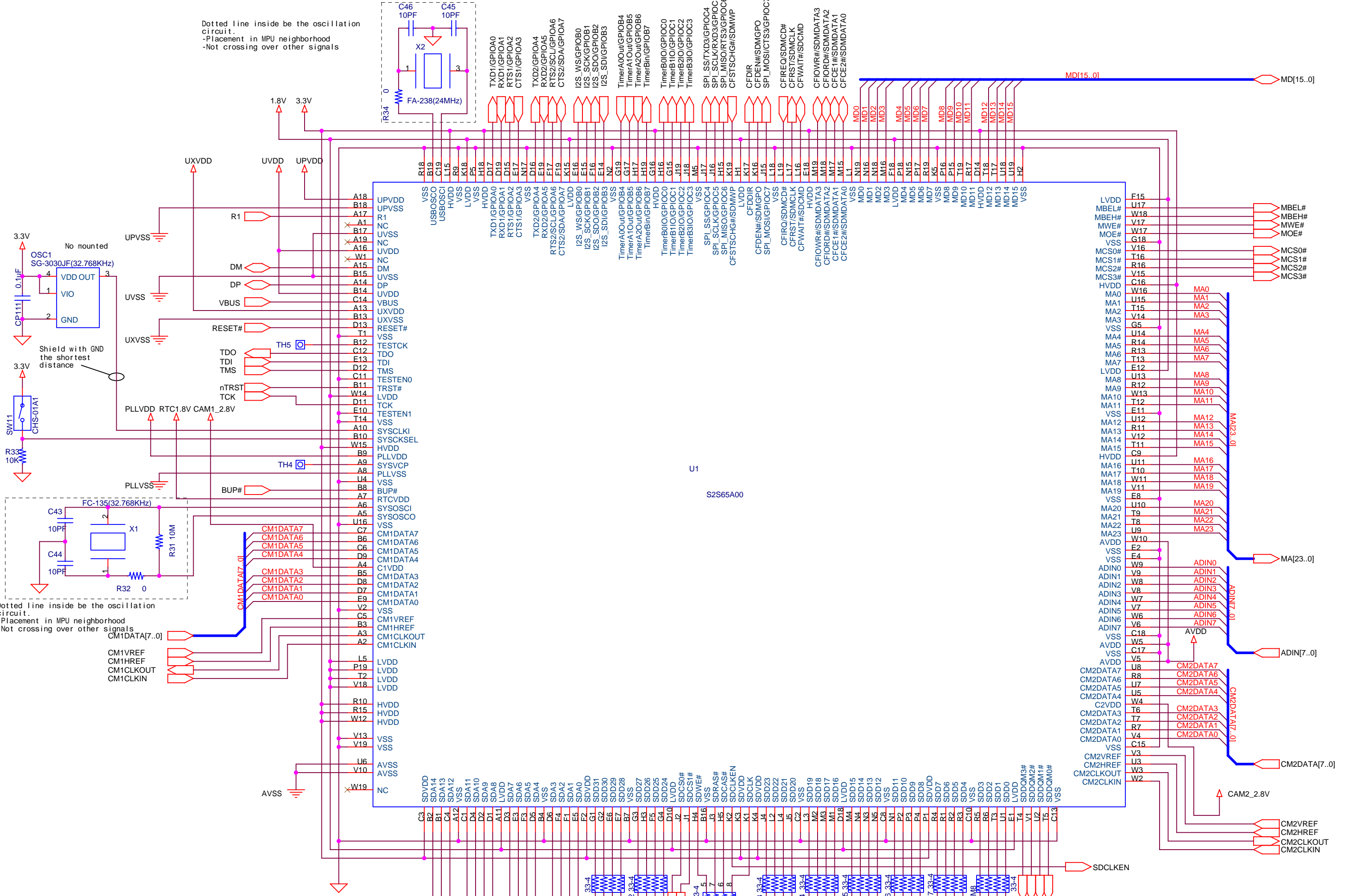


--- HISTORY ---
 14.JTAG&RESET Rev.1.00 -> 1.10 :JTAG nSTST

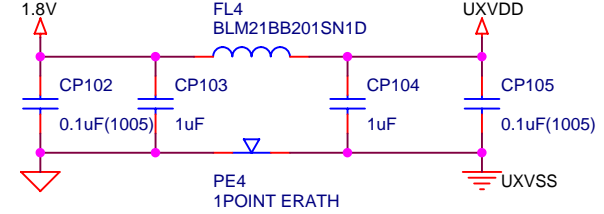
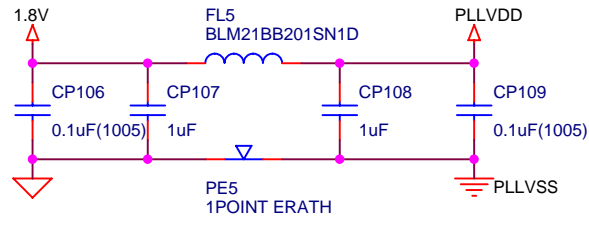
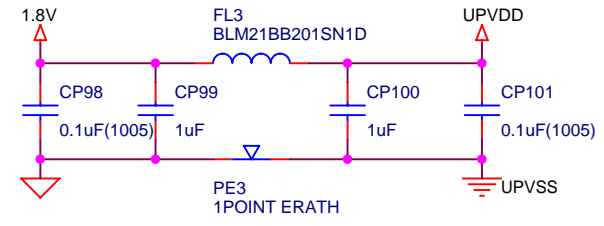
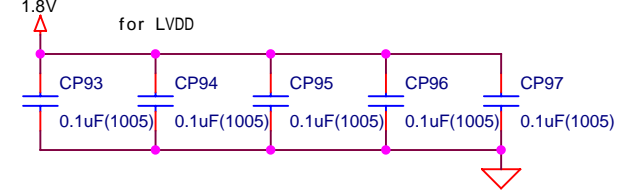
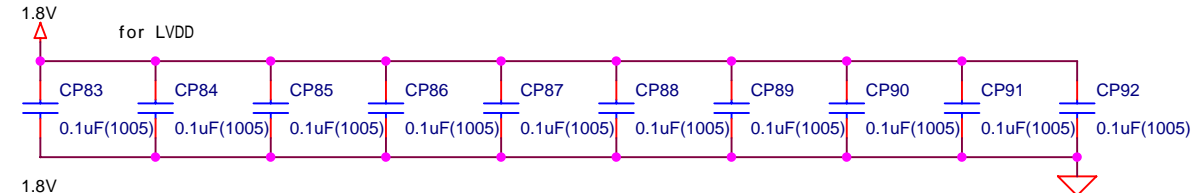
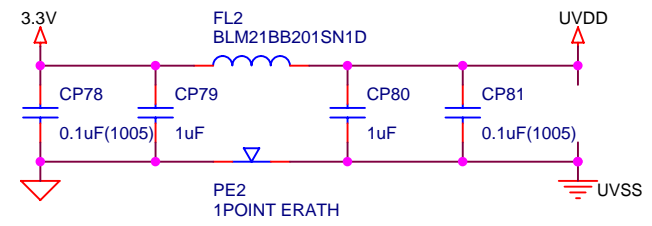
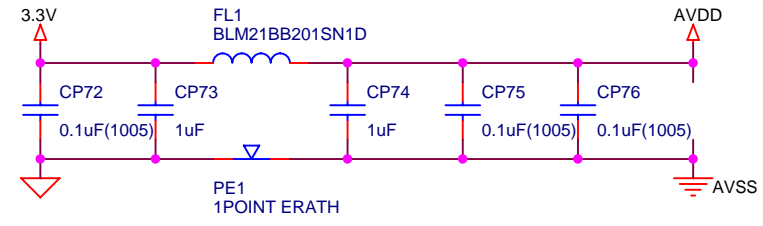
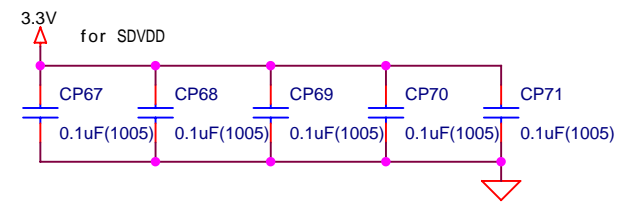
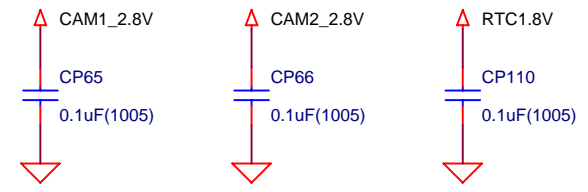
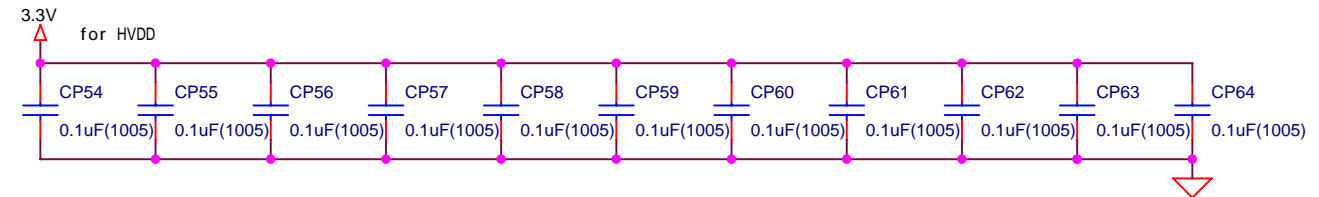
S2S65A00 Evaluation Board		
Title: TOP		
Size: A3	Document Number: 06A-00373-01	Rev: 1.10
Date: Monday, July 02, 2007	Sheet: 1	of 16



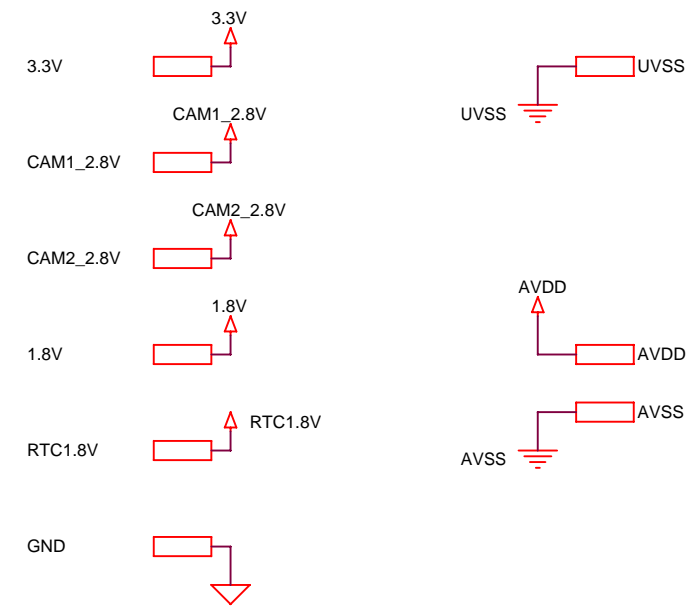
Dotted line inside be the oscillation circuit.
 -Placement in MPU neighborhood
 -Not crossing over other signals

Dotted line inside be the oscillation circuit.
 -Placement in MPU neighborhood
 -Not crossing over other signals

S2S65A00 Evaluation Board Title MPU(1/2)		
Size A3	Document Number 06A-00373-01	Rev 1.00
Date: Thursday, April 24, 2008	Sheet 2	of 16



*PE is dummy parts



S2S65A00 Evaluation Board		
Title MPU(2/2)		
Size A3	Document Number 06A-00373-01	Rev 1.00
Date: Thursday, April 24, 2008	Sheet 3	of 16

MPU

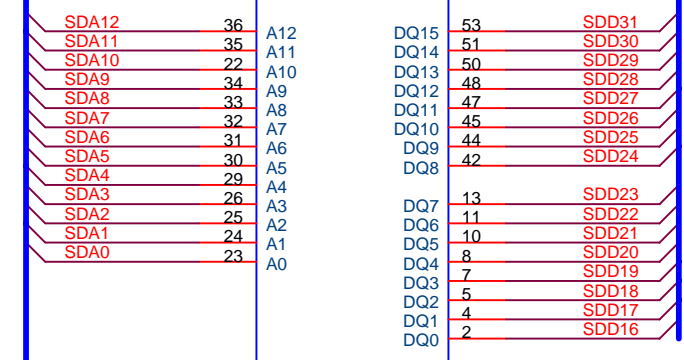
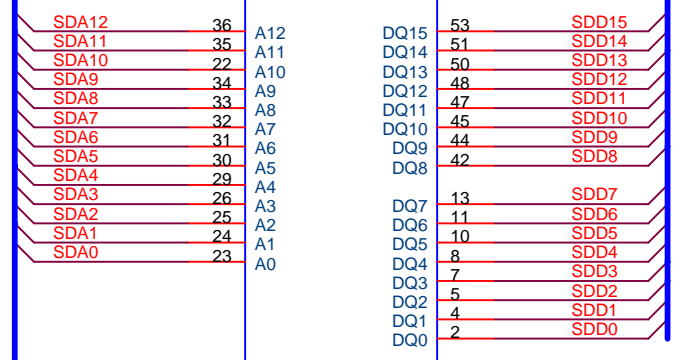
SDA[14..0]
SDD[31..0]

SDA14..0

SDD[31..0]

U2 MT48LC32M16A2P-75

U3 MT48LC32M16A2P-75



SDDQM1#
SDDQM0#

SDRAS#
SDCAS#
SDWE#
SDA14
SDA13
SDDQM1#
SDDQM0#

SDDQM3#
SDDQM2#

SDRAS#
SDCAS#
SDWE#
SDA14
SDA13
SDDQM3#
SDDQM2#

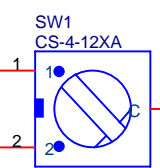
SDCLK
SDCLKEN

SDCLK
SDCLKEN

SDRAS#
SDCAS#
SDWE#

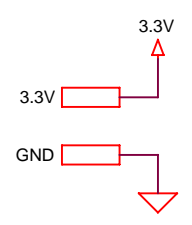
SDCS0#
SDCS1#

SDCLK
SDCLKEN



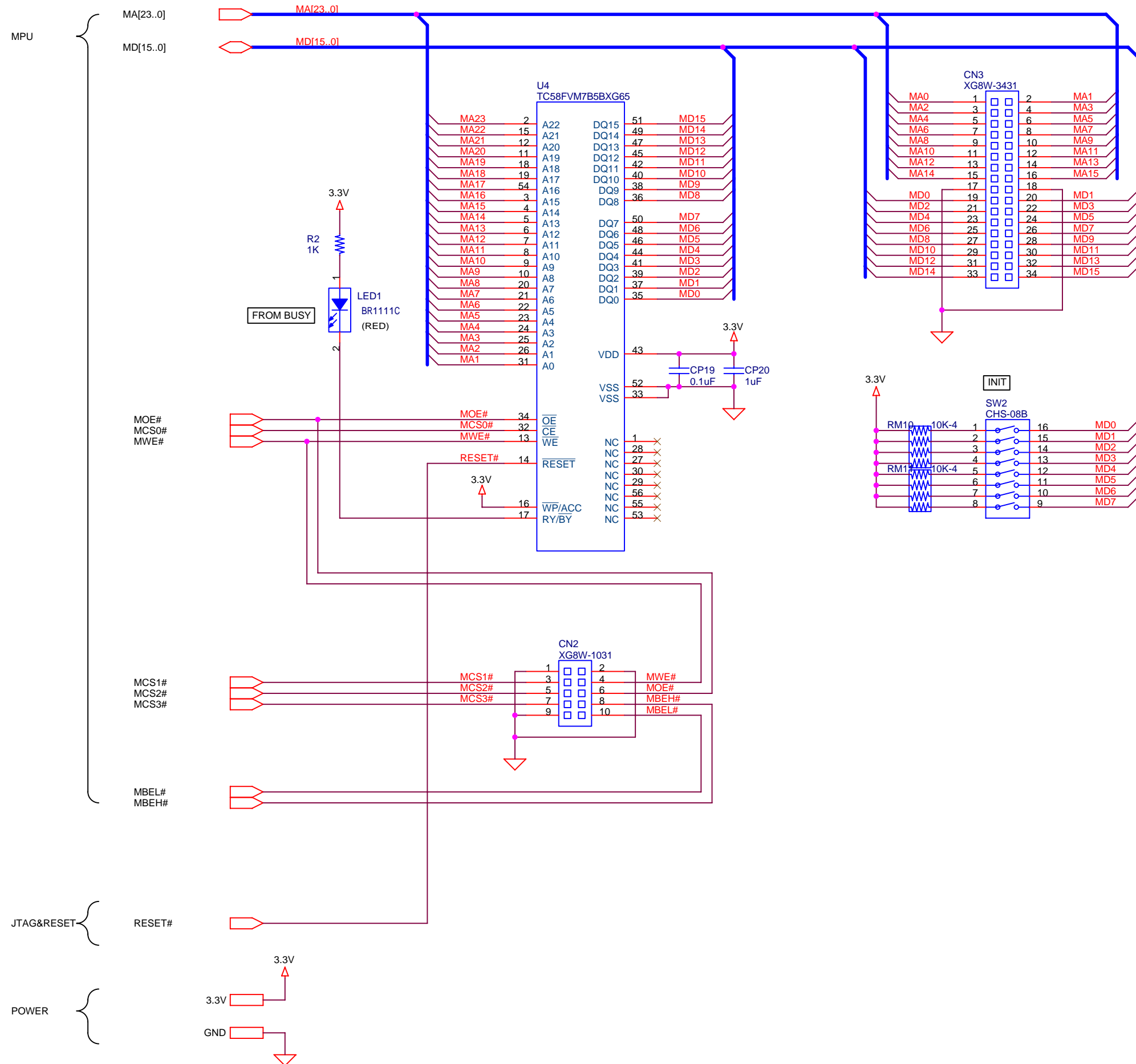
GND shield

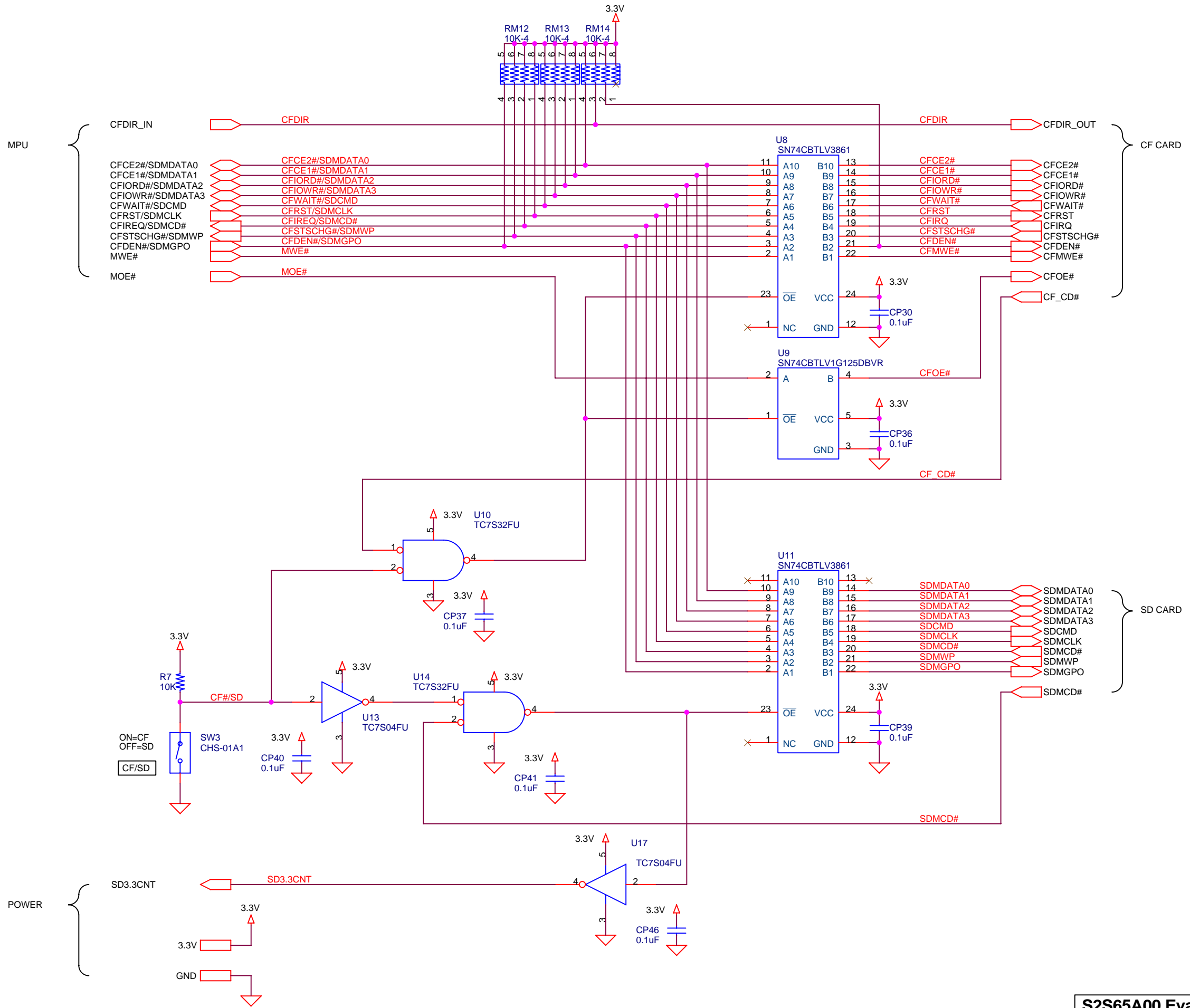
POWER

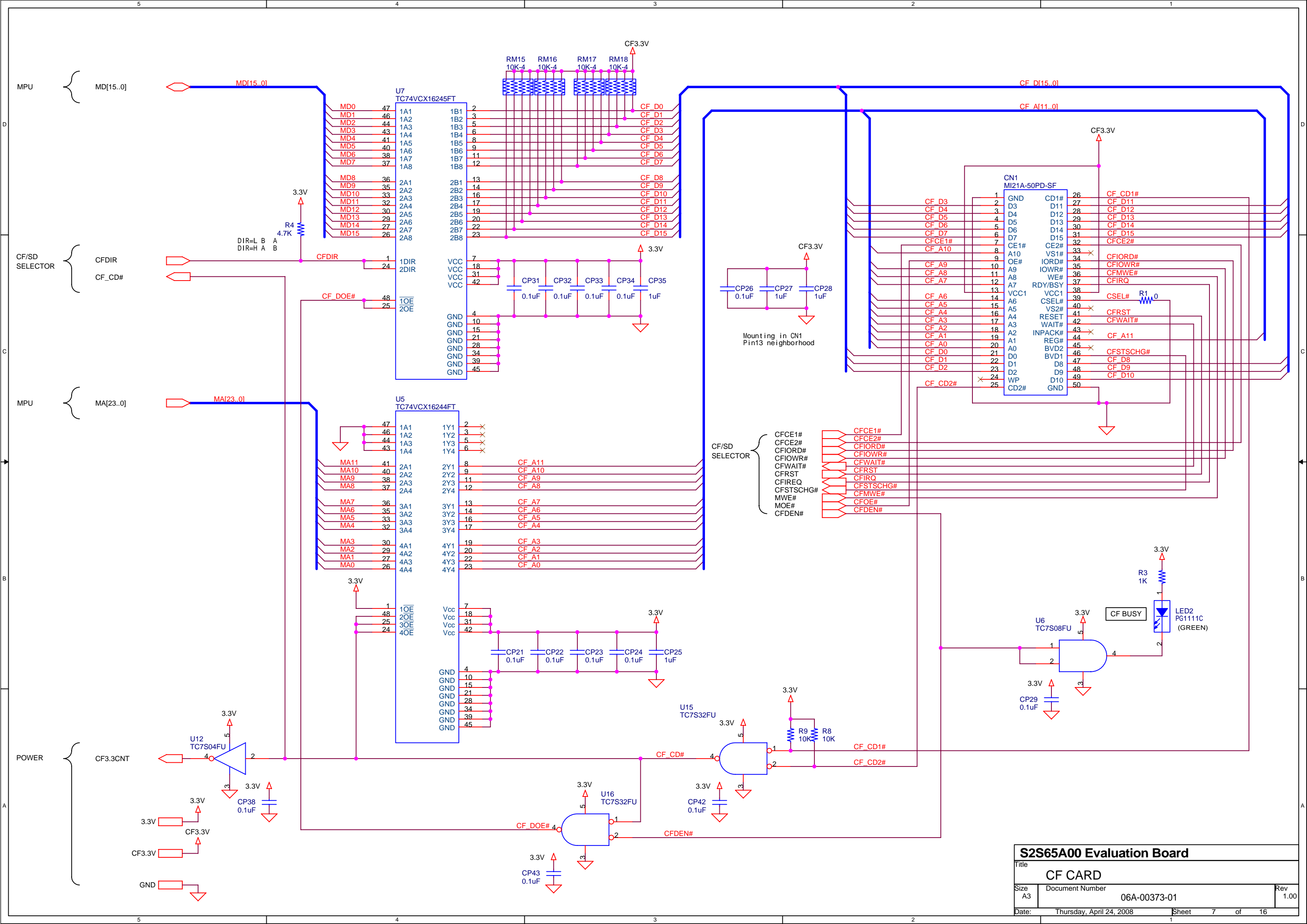


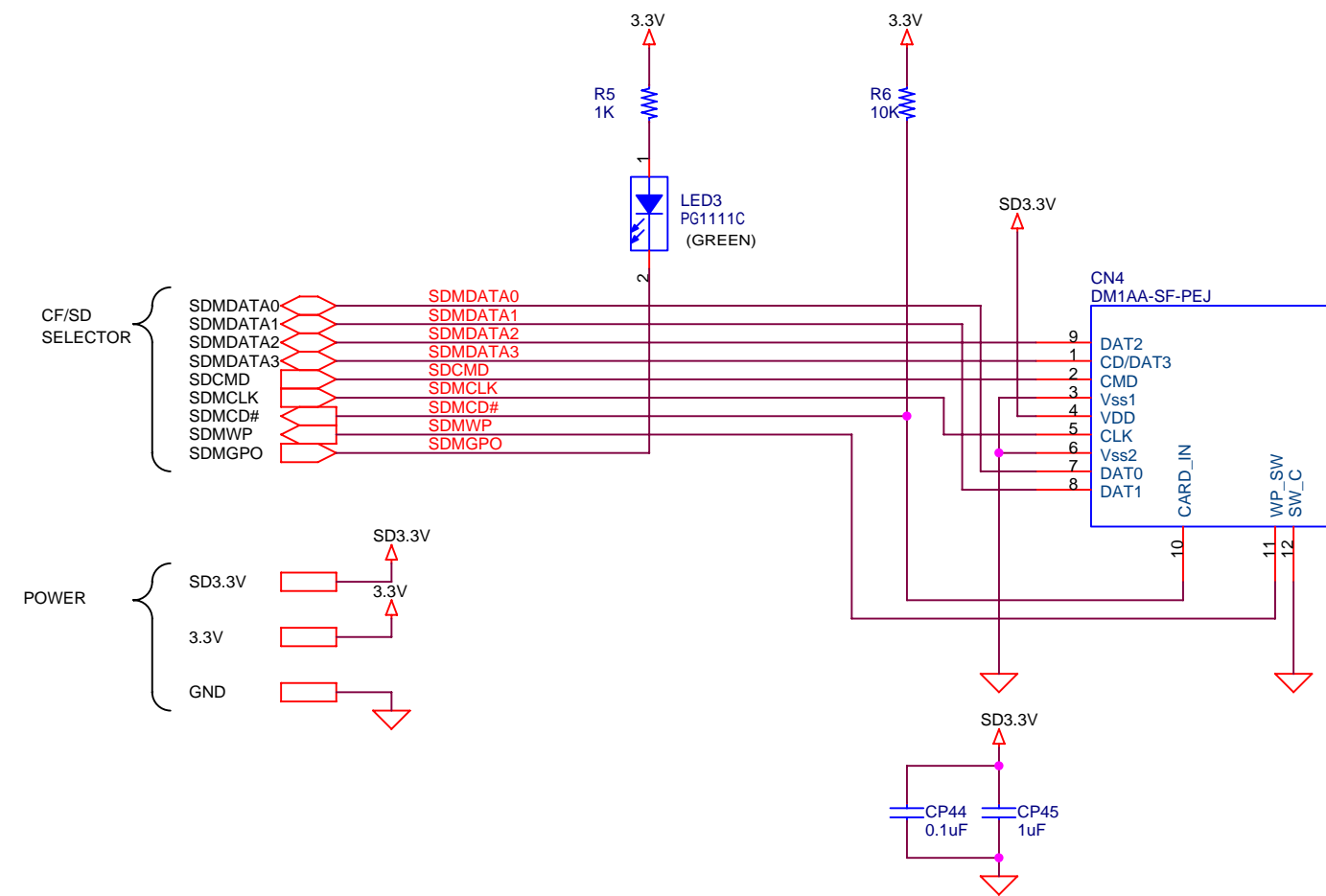
S2S65A00 Evaluation Board

Title		
SDRAM		
Size	Document Number	Rev
A3	06A-00373-01	1.00
Date:	Thursday, April 24, 2008	Sheet 4 of 16

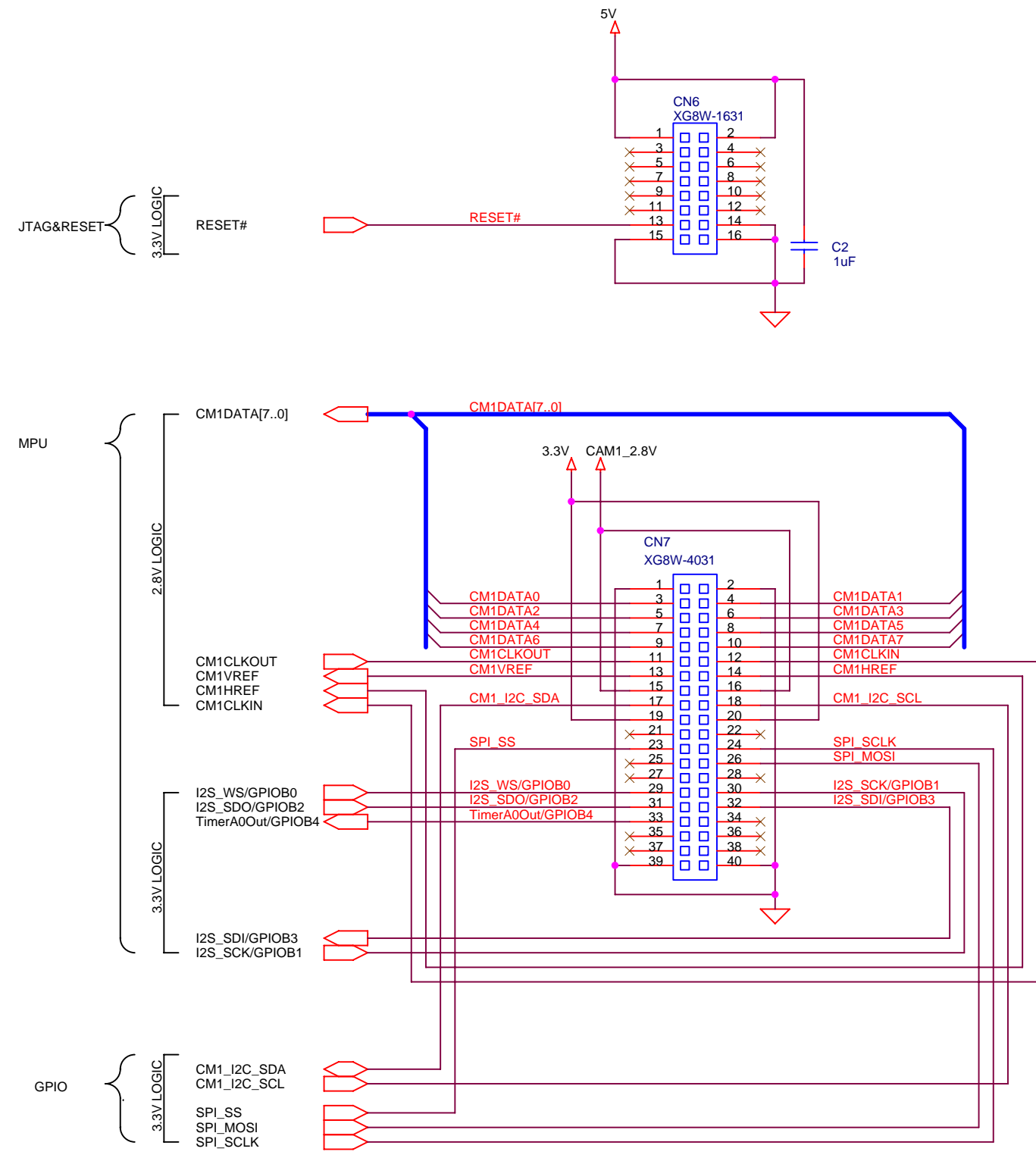




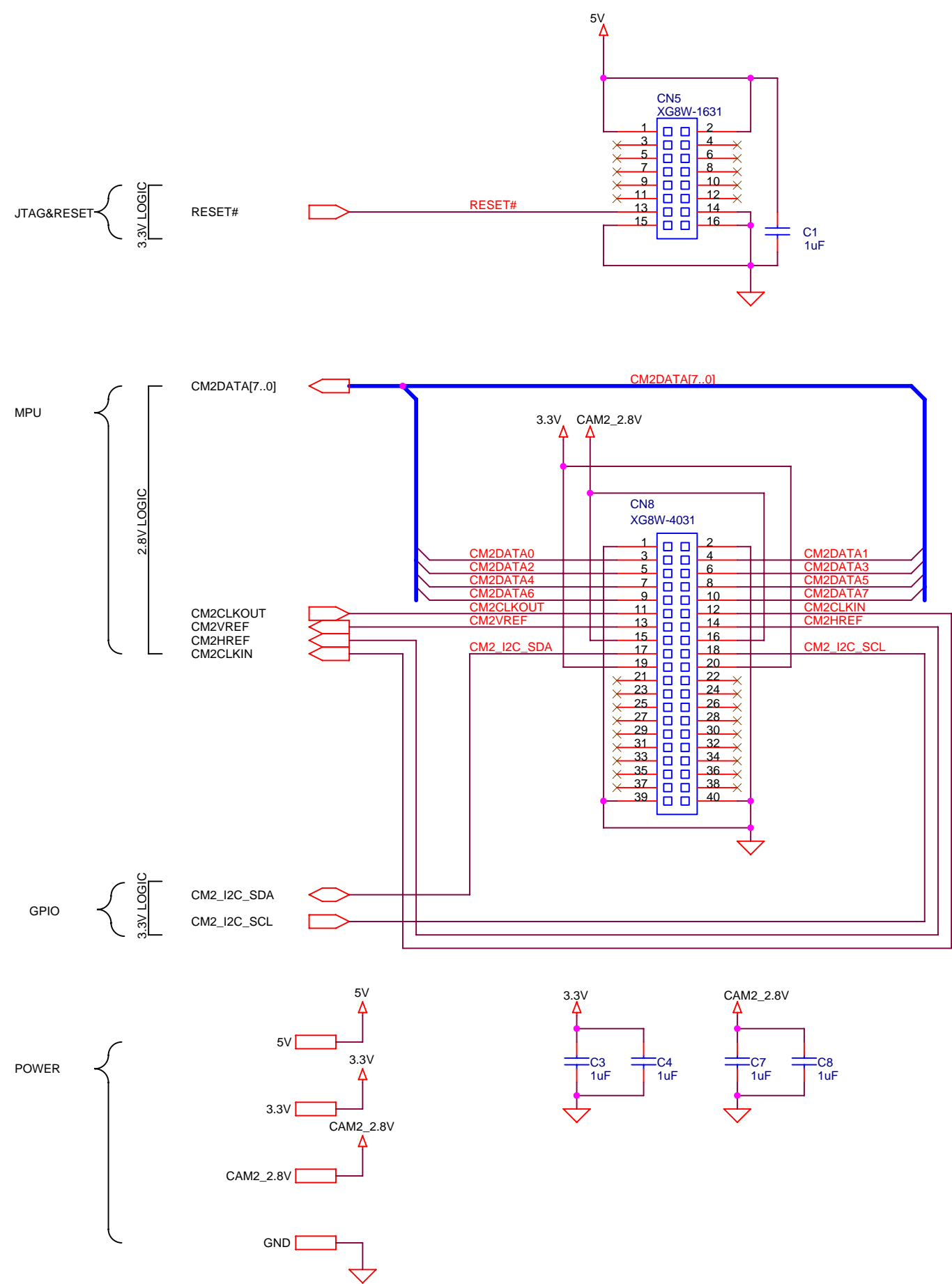




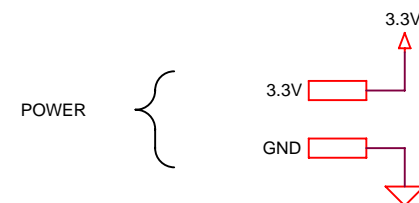
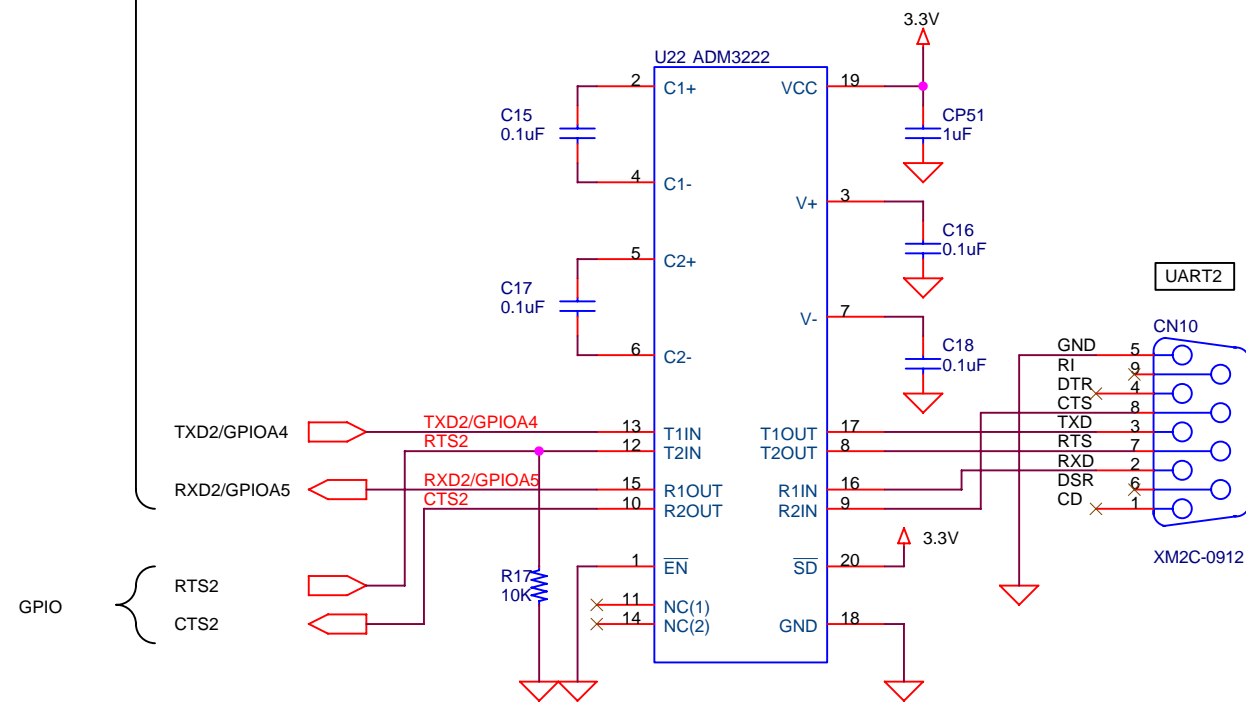
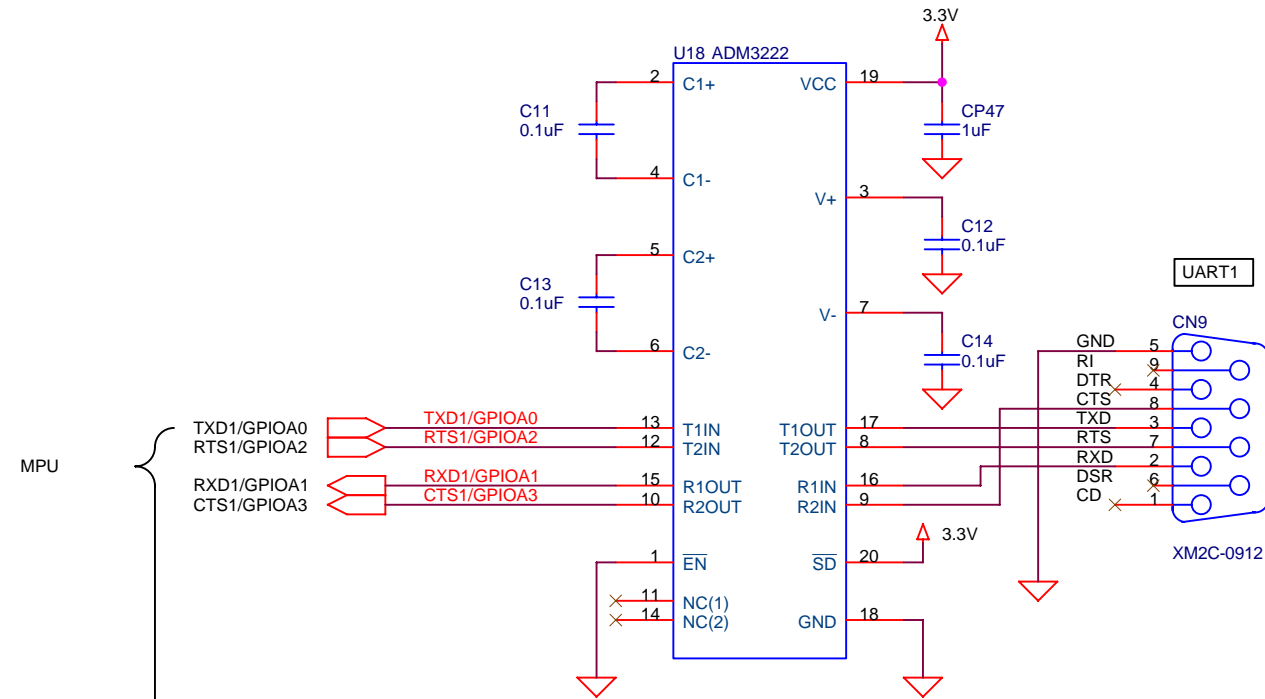
S2S65A00 Evaluation Board		
Title SD CARD		
Size A3	Document Number 06A-00373-01	Rev 1.00
Date: Thursday, April 24, 2008	Sheet 8	of 16



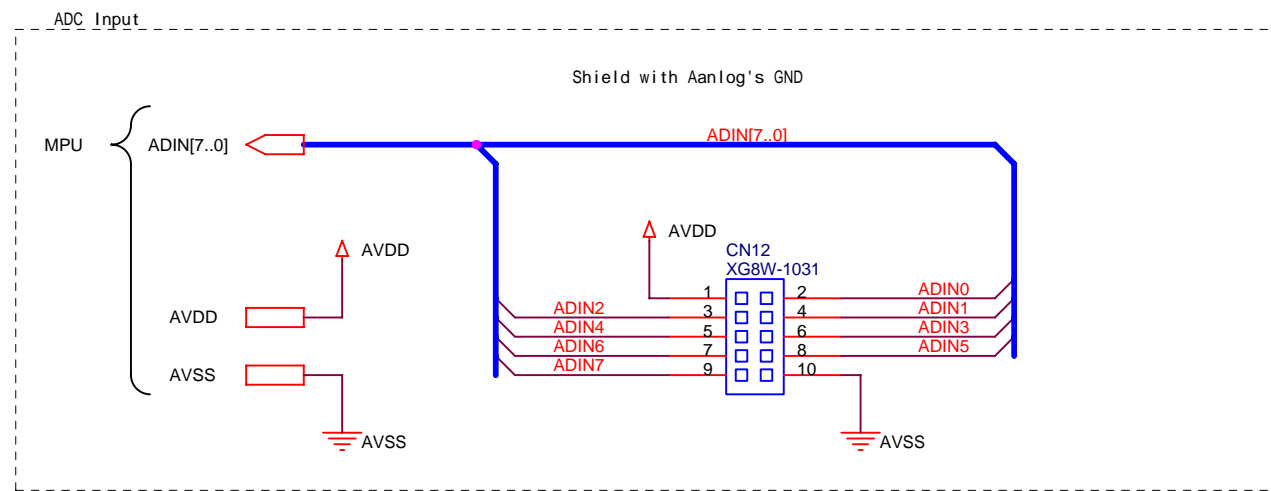
S2S65A00 Evaluation Board		
Title		
CAMERA1		
Size	Document Number	Rev
A3	06A-00373-01	1.00
Date:	Thursday, April 24, 2008	Sheet 9 of 16



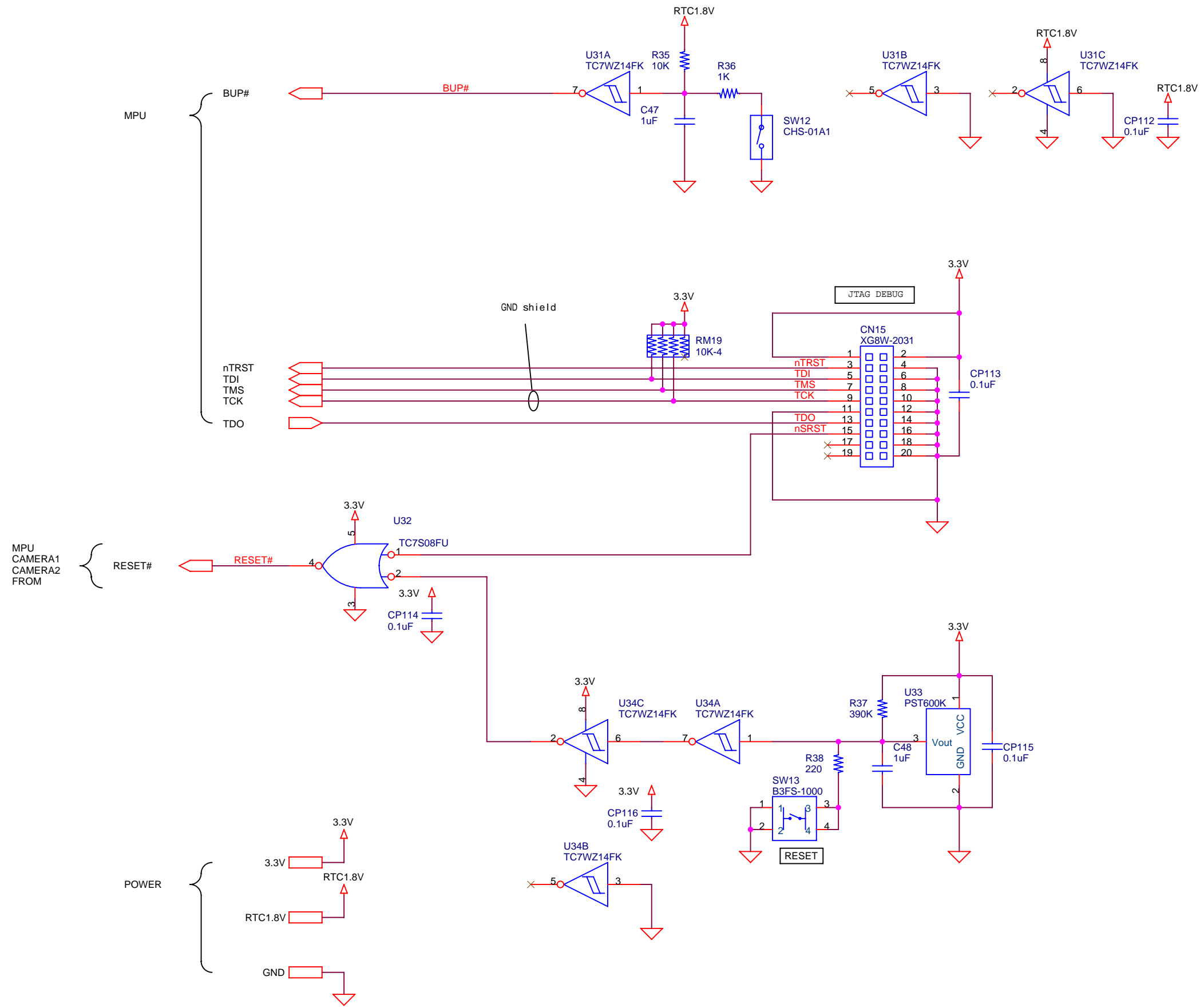
S2S65A00 Evaluation Board		
Title CAMERA2		
Size A3	Document Number 06A-00373-01	Rev 1.00
Date:	Thursday, April 24, 2008	Sheet 10 of 16



S2S65A00 Evaluation Board		
Title		
UART		
Size	Document Number	Rev
A3	06A-00373-01	1.00
Date:	Thursday, April 24, 2008	Sheet 11 of 16

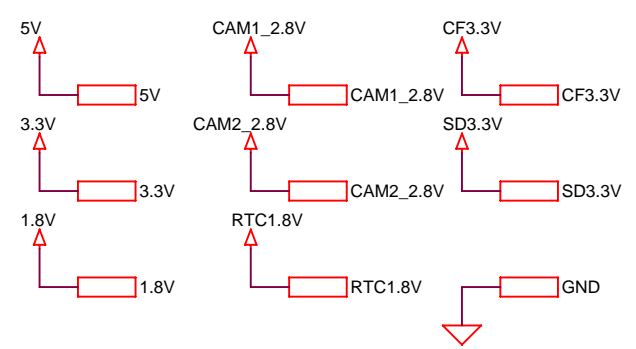
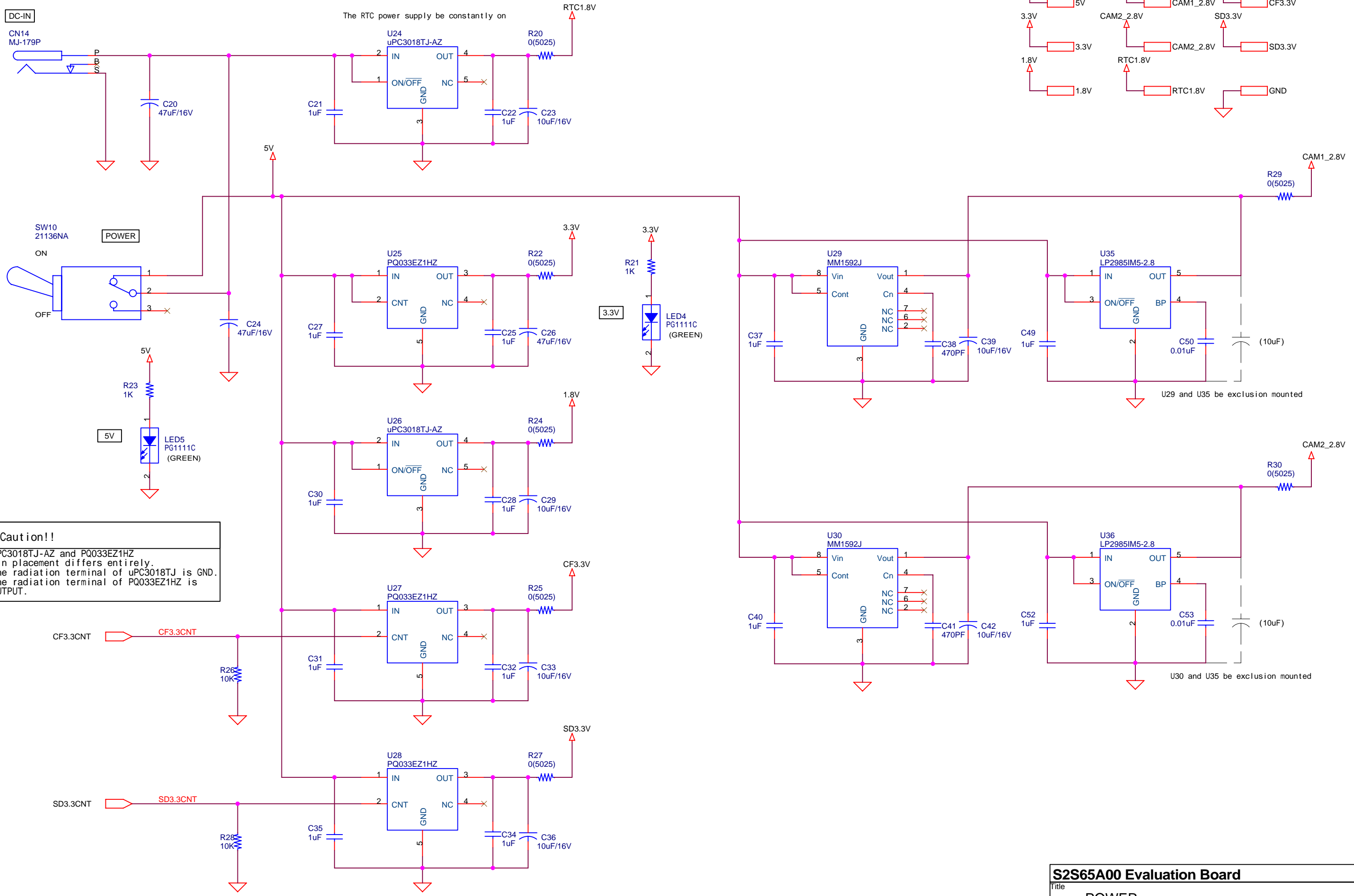


S2S65A00 Evaluation Board		
Title ADC		
Size A3	Document Number 06A-00373-01	Rev 1.00
Date:	Thursday, April 24, 2008	Sheet 13 of 16



--- HISTORY ---
 14.JTAG&RESET Rev.1.00 -> 1.10 :JTAG nSTST

S2S65A00 Evaluation Board		
Title RESET&CLOCK		
Size A3	Document Number 06A-00373-01	Rev 1.10
Date: Monday, July 02, 2007	Sheet 15	of 16



Caution!!
 UPC3018TJ-AZ and PQ033EZ1HZ
 Pin placement differs entirely.
 The radiation terminal of uPC3018TJ is GND.
 The radiation terminal of PQ033EZ1HZ is
 OUTPUT.

S2S65A00 Evaluation Board		
Title		
POWER		
Size	Document Number	Rev
A3	06A-00373-01	1.00
Date:	Thursday, April 24, 2008	Sheet 16 of 16