S5U1C17701T1100 (SVT17701)

EPSON
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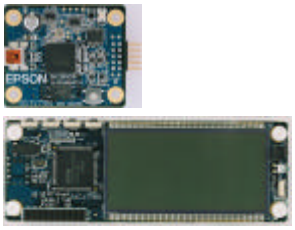
Software eValuation Tool for S1C17701



- STN LCD panel
  (display size: 32 commons × 56 segments, black & white)
- Infrared LED/detecting unit
- Key input circuit (four keys available)
- Reset switch
- Extended interface connectors (P, UART, SPI and I2C ports)
- ICD board connector

■ DESCRIPTIONS

S5U1C17701T1100 (Software eValuation Tool for S1C17701; hereinafter referred to as SVT17701) is an evaluation board for the SEIKO EPSON MCU S1C17701. The SVT17701 consists of two circuit boards: CPU board and ICD board. By connecting these boards, the SVT17701 allows the user to debug the application software without an ICD or other debugging tools. In addition, it provides extended interfaces such as serial ports allowing the user to connect an expansion board.

■ FEATURES

● CPU board

CPU	S1C17701
Input power voltage	+3.3V (DC) (supplied through the ICD interface or a CR2032 coin cell can be used)
CPU input clock	OSC1: 32.768kHz OSC3: 8MHz
On-board functions/devices	- STN LCD panel (display size: 32 commons × 56 segments, black & white) - Infrared LED/detecting unit - Key input circuit (four keys available) - Reset switch - Extended interface connectors (P, UART, SPI and I2C ports) - ICD board connector

● ICD board

CPU interface	USB 1.1
Power voltage	USB bus power (On-board regulator output voltage: +3.3 V)
On-board functions/devices	- Status indicator LED - Reset switch - CPU board connector

■ Users site list of sample programs

Oscillator	SPI Master Mode
I/O Ports	SPI Slave Mode
16-bit Timer	I^2C Communications
8-bit Timer	REMC Transmission
PWM & Capture Timer	REMC Reception
8-bit OSC1 Timer	LCD Driver
Clock Timer	Supply Voltage Detector
Stopwatch Timer	TOPPERS_OSEK_17
Watchdog Timer	Self-Programming
UART	Sleep/Halt Command

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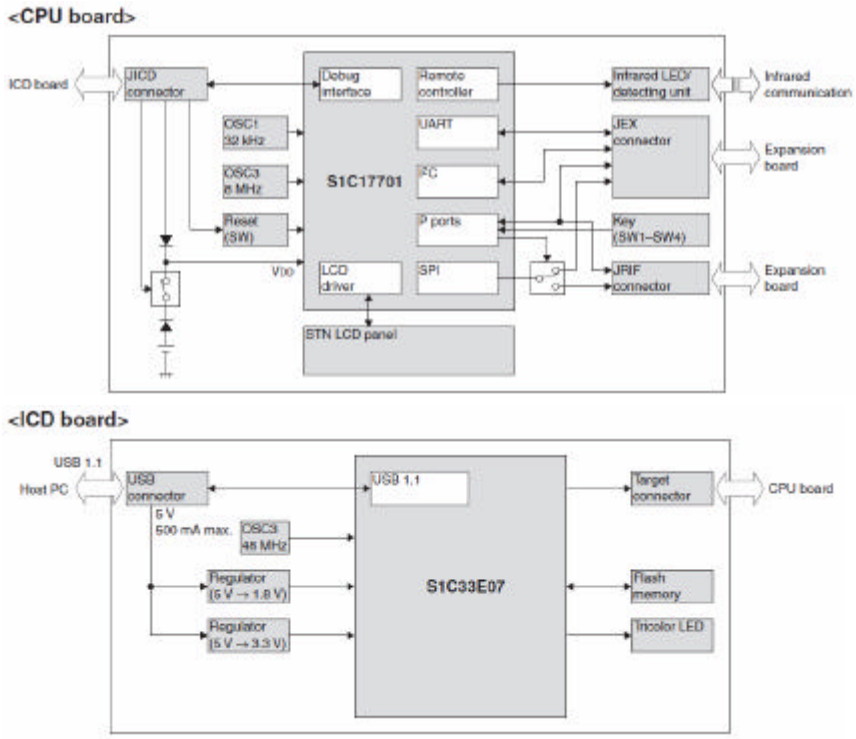
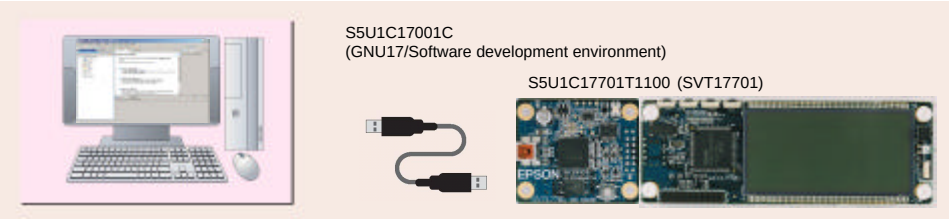
■ Block Diagram



■ Connection Diagram



SEIKO EPSON CORPORATION

SEMICONDUCTOR OPERATIONS DIVISION

IC Sales Department
IC International Sales Group
421-8 Hino, Hino-shi, Tokyo 191-8501, JAPAN
Phone: +81-42-587-5814 FAX: +81-42-587-5117

■ EPSON Electronic devices Website

http://www.epson.jp/device/semicon_e/

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