

## Product features

- Automotive standards-compliant eDP to OpenLDI interface conversion IC
- Extensive safety functions (SPI connection, CRC, blank screen)
- Splitter function that enables video of the same resolution up to full-HD to be simultaneously output on two displays

## Product specifications

Model No.	S2D13V70
Supply voltage	3.3 V (I/O)
	Internal core voltage (1.8 V)
Input interface	eDP 1.4a x 2 lanes, up to 2.7 Gbps/lane
Output interface	OpenLDI-Tx x 2 channels, up to 120 MHz/ch
Display resolution	Up to 1920 x 1200 @ 60 fps
Image correction	8-bit digital gamma LUT + 2-bit dithering
Safety functions	SPI connection (checksum with 16-Byte cache)
	Input pixel clock/Sync signal monitoring
	CRC (full screen/register setting/frame comparison)
	Blank screen
Automotive standard conformance	AEC-Q100
Operating temperature range	-40 to +105°C
Other	System clock: 48 MHz
	Splitter function
Package	PFBGA8-81 (8 mm × 8 mm × 1.2 mm, with a pin pitch of 0.8 mm)