LCD Drivers Spec Tables

■ Drivers for small and medium-sized panel

S1D15000 series

Products	Supply voltage range (V)	LCD voltage range (V)	Duty	Segment	Common	Display RAM (bits)	Micro processor interface	Frequency (kHz)	Package	Remarks
S1D15E00D01B	1.8 to 3.6	3.2 to 10	1/100	132	100	132×100 bits	8-bit parallel / Serial	40	Au bump chip	4-line MLS driving
S1D15710D10B	1.8 to 5.5	4.5 to 18	1/65	224	65	224×65 bits	8-bit parallel / Serial	22	Au bump chip	Built-in power circuit for LCD, High power voltage follower version
S1D15711D00B	1.8 to 5.5	4.5 to 9	1/9	200	9	200×9 bits	8-bit parallel / Serial	46	Au bump chip	Built-in power circuit for LCD
S1D15712D01B	2.7 to 5.5	5.6 to 16.2	1/81	256	81	256×81×2 bits	8-bit parallel / Serial	400	Au bump chip	Built-in power circuit for LCD 4-line MLS driving 4-gray scale
S1D15714D01E	2.7 to 5.5	V_{DD} to 16	1/65	168	65	168×65 bits	8-bit parallel / Serial	100	Au bump chip	Built-in power circuit for LCD 4-line MLS driving
S1D15715D00B	1.8 to 5.5	4.5 to 9.0	1/17	102	17	102×33 bits	8-bit parallel / Serial	21.76	Au bump chip	Built-in power circuit for LCD
S1D15716D00B			1/9		9			23.04	Au bump chip	
S1D15719D22B	2.7 to 5.5	5.6 to 25	1/132	180	132	180×132 bits	8-bit parallel / Serial	4896	Au bump chip	Built-in power circuit for LCD 4-line MLS driving 4-gray scale
S1D15721D01B	2.7 to 5.5	5.6 to 16.2	1/81	256	81	256×81 bits	8-bit parallel / Serial	420	Au bump chip	Built-in power circuit for LCD 4-line MLS driving LCD drive voltage 4-gray scale
S1D15722D01B	2.7 to 5.5	15 to 25	1/184	224	184	224×184×2 bits	8-bit parallel / Serial	640	Au bump chip	External bias input required 4-line MLS driving LCD drive voltage 4-gray scale
S2D15730D00B	2.7 to 5.5	11 to 27	1/132	180	132	180×144×2 bits	8-bit parallel / Serial	2000	Au bump chip	Built-in power circuit for LCD 4-line MLS driving 4-gray scale
S2D15731D00B	2.7 to 5.5	11 to 27	1/132	256	132	256×160×2 bits	8-bit parallel / Serial	2000	Au bump chip	Built-in power circuit for LCD 4-line MLS driving 4-gray scale
S2D15102D00B	2.7 to 5.5	3.6 to 7.0	1/1 to 1/4	80	4	80×4 bits	Serial	Variable	Au bump chip	Built-in bias circuit for LCD panel

S2D19600 Series (1-chip monochrome TFT driver)

Products	Supply voltage (V)	LCD drive voltage (V)	Gate drive voltage (V)	Source output	Gate output	Data RAM capacity	MPU interface	Oscillation frequency (MHz)	Package	Remarks
S2D19600D00B	2.7 to 5.5	to 5.5	to 32	320	320	320×320 ×4 bits	8-bit Parallel/ serial	1	Au bump chip	Built-in power circuit for LCD panel

■ STN LCD Drivers for large panel

S1D17000 series

● Selectable Segment or Common driver

Products	Supply voltage Range (V)	LCD voltage range (V)	Duty	Outputs	Data bus	Package	Remarks
S1D17A03D00B	2.4 to 5.5	8 to 40	~1/480	160	4-/8-bit parallel	Au bump chip	Pin input enables to select the common or segment driver.
S1D17A04D00B	2.4 to 5.5	8 to 40	~1/480	240	4-/8-bit parallel	Au bump chip	Pin input enables to select the common or segment driver.

● Segment driver (S1D17A08) and common driver (S1D17E02) for COG

Products	Supply voltage Range (V)	LCD voltage range (V)	Duty	Outputs	Data bus	Package	Remarks
S1D17E02D00B	2.5 to 5.5	15 to 45	~1/240	240	-	Au bump chip	For COG. APT driving method common driver
S1D17A08D00B	2.5 to 5.5	2.6 to 5.5	~1/240	320	4-/8-bit parallel	Au bump chip	For COG. APT driving method segment driver