

Setting example of SDRAM Memory Controller Register  
for S5U13513P00C100 evaluation board

S5U13513P00C100 setting: JP11 2-3 connected: 16-bit data bus (16MB)

Memory type: **IS42S16800D-7TL (ISSI)**

REG	Default value	Recommended setting	Note
1C00h	0000h	0011h	Fixed value
1C02h	0002h	0003h	Data width = 16bit
1C04h	7AAFh	365Ah	
1C06h	C010h	8010	Memory size = 128Mbit
1C08h	0F1Ah	0F1A	Default value
1C0Ah	0000h	0000	Default value
1C0Ch	0411h	0411	Default value
1C0Eh	-	-	Reserved Register
1C10h	0000h	0000	Default value
1C12h	4000h	0000	Default value
1C14h	0000h	0000	Default value

S5U13513P00C100 setting: JP11 2-3 connected: 16-bit data bus (32MB)

Memory type: **IS42S16800D-7TL (ISSI)**

REG	Default value	Recommended setting	Note
1C00	0000h	0011h	Fixed value
1C02	0002h	0005h	Data width = 32bit
1C04	7AAFh	365Ah	
1C06	C010h	8010h	Memory size = 128Mbit
1C08	0F1Ah	0F1Ah	Default value
1C0A	0000h	0000h	Default value
1C0C	0411h	0411h	Default value
1C0E	-	-	Reserved Register
1C10	0000h	0000h	Default value
1C12	4000h	0000h	Default value
1C14	0000h	0000h	Default value